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Vishay Semiconductors

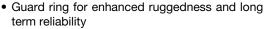
High Performance Schottky Rectifier, 1.0 A



PRODUCT SUMMARY			
Package	SMB		
I _{F(AV)}	1 A		
V _R	90 V, 100 V		
V _F at I _F	0.78 V		
I _{RM}	1 mA at 125 °C		
T _J max.	175 °C		
Diode variation	Single die		
E _{AS}	1.0 mJ		

FEATURES

- · Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation





- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-MBRS190-M3, VS-MBRS1100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	А	
V _{RRM}		90, 100	V	
I _{FSM}	t _p = 5 μs sine	870	А	
V _F	1.0 A _{pk} , T _J = 125 °C	0.63	V	
TJ	Range	-55 to +175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRS190-M3	VS-MBRS1100-M3	UNITS
Maximum DC reverse voltage	V_R	90	100	V
Maximum working peak reverse voltage	V_{RWM}	90	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 147 °C, rectangular waveform		1.0	
Maximum peak one cycle	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	870	Α
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	50	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.5 \text{A}, L = 8 \text{mH}$		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical 0.5		Α	



VS-MBRS190-M3, VS-MBRS1100-M3

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V (1)	1 A	T _J = 25 °C	0.78	V
See fig. 1	V _{FM} ⁽¹⁾ 1 A	V _{FM} (i) I A	T _J = 125 °C	0.62]
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V Datad V	0.5	- mA
See fig. 2	IRM (*)	T _J = 125 °C	V _R = Rated V _R	1.0	
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		42	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar to DO-214AA)	19/	′10

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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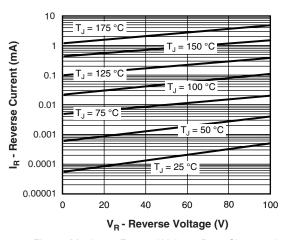


Fig. 1 - Maximum Forward Voltage Drop Characteristics

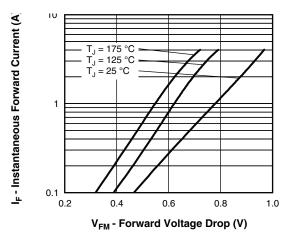


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

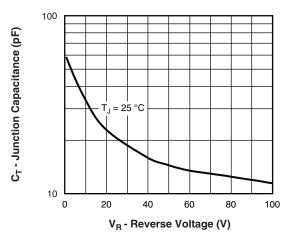


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

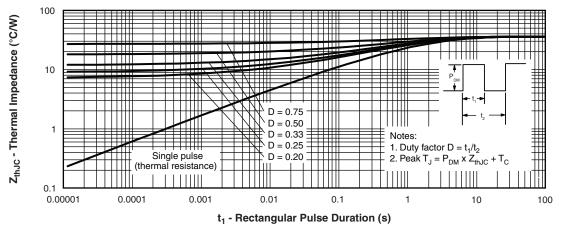


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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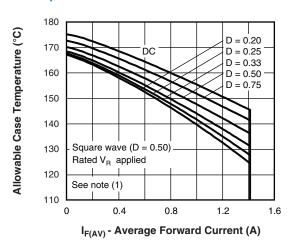
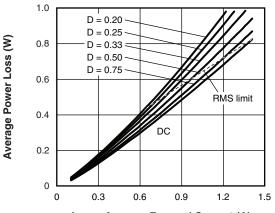


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

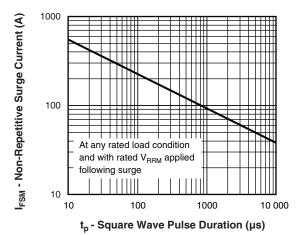


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

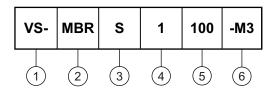
 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = 80 \ \% \ \text{rated} \ V_R \\ \end{array}$

VS-MBRS190-M3, VS-MBRS1100-M3

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Schottky MBR series

3 - S = SMB

4 - Current rating (1 = 1 A)

90 = 90 V 100 = 100 V

6 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBRS190-M3/5BT	5BT	3200	13" diameter plastic tape and reel		
VS-MBRS1100-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95401</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95516			

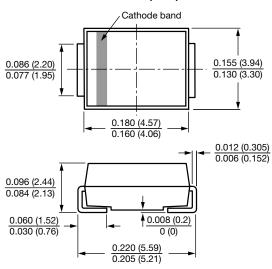


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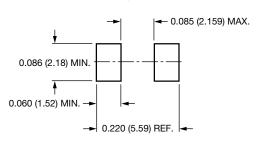
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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