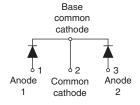


High Performance Schottky Rectifier Gen 3, D-61 Package, 2 x 40 A

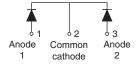
VS-83CNQ...APbF





VS-83CNQ...ASMPbF



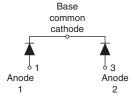


D-61-8-SM

VS-83CNQ...ASLPbF







PRODUCT SUMMARY				
Package	D-61			
I _{F(AV)}	2 x 40 A			
V _R	80 V, 100 V			
V _F at I _F	0.81			
I _{RM} max.	35 mA at 125 °C			
T _J max.	175 °C			
Diode variation	Common cathode			
E _{AS}	15 mJ			

FEATURES

- 175 °C T_J operation
- · Center tap module
- Low forward voltage drop
- · High frequency operation
- · High power discrete
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- · New fully transfer-mold low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS VALUES UNIT					
I _{F(AV)}	Rectangular waveform	80	А			
V _{RRM}		80, 100				
I _{FSM}	$t_p = 5 \mu s sine$	$_{0} = 5 \mu s sine$ 7000				
V _F	40 A _{pk} , T _J = 125 °C (per leg)	0.67	V			
T _J	Range	-55 to +175	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-83CNQ080APbF	VS-83CNQ100APbF	UNITS
Maximum DC reverse voltage	V_{R}	80	100	V
Maximum working peak reverse voltage	V_{RWM}	- 60 100 V		V



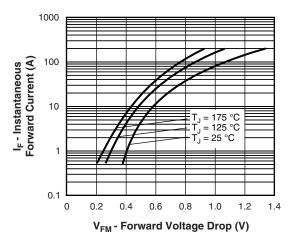
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C	, rectangular waveform	80	
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated	7000	Α
surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	720	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1 \text{A}$, $L = 30 \text{mH}$		15	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А

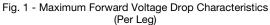
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	40 A	T _J = 25 °C	0.81	- V
		80 A		1.00	
		40 A	T _J = 125 °C	0.67	
		80 A		0.82	
Maximum reverse leakage current per leg		T _J = 25 °C	V _R = Rated V _R	1.5	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = nateu v _R	35	IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.5		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C
Maximum thermal	per leg	D	DC operation, see fig. 4	0.85	°C/W
resistance, junction to case	per package	R _{thJC}	DC operation	0.42	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	3,11
Approximate weight				7.8	g
Approximate weight	Approximate weight			0.28	oz.
Mounting torque	minimum		Recommended hardware 3M stainless screw	12 (10)	kgf · cm
Mounting torque	maximum			24 (20)	(lbf · in)
Marking device			Case style D-61	83CNQ080A	
				83CNQ100A	
		Occupate to D 04 0 0M	83CNQ080ASM		
			Case style D-61-8-SM	83CNQ100ASM	
		Coop et de D. 61, 9, Cl	83CNQ080ASL		
			Case style D-61-8-SL		83CNQ100ASL





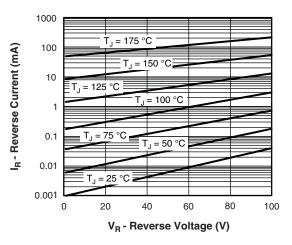


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

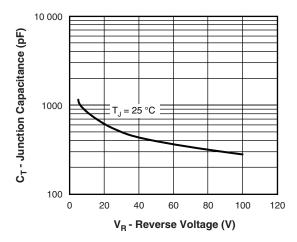


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

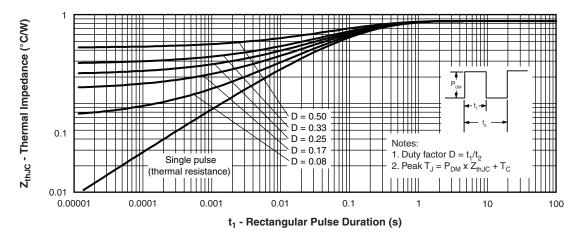


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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Vishay Semiconductors

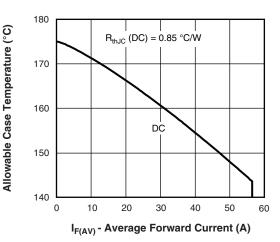


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

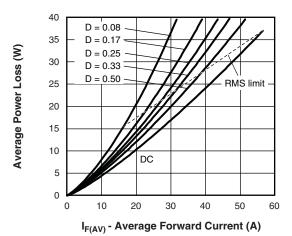


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

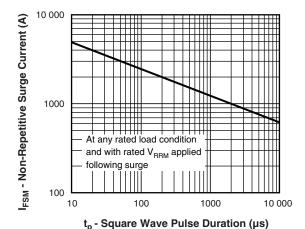


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

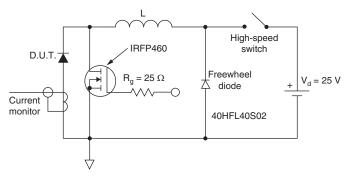
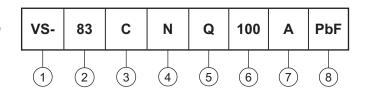


Fig. 8 - Unclamped Inductive Test Circuit



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (80 A)

3 - Circuit configuration:

C = common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage ratings - 080 = 80 V 100 = 100 V

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = standard production

• PbF = lead (Pb)-free

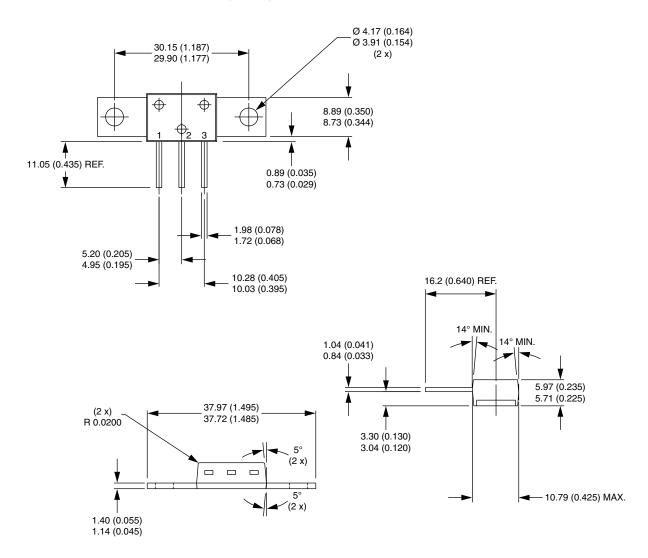
Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95354</u>					
Part marking information	www.vishay.com/doc?95356				
SPICE model	www.vishay.com/doc?95290				



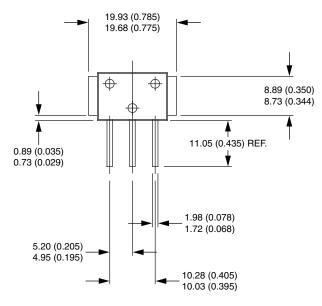
D-61-8, D-61-8-SM, D-61-8-SL

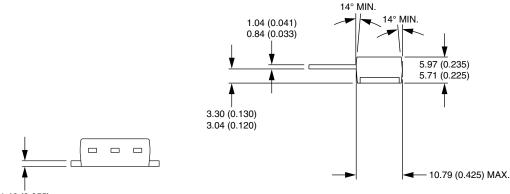
DIMENSIONS - D-61-8 in millimeters (inches)





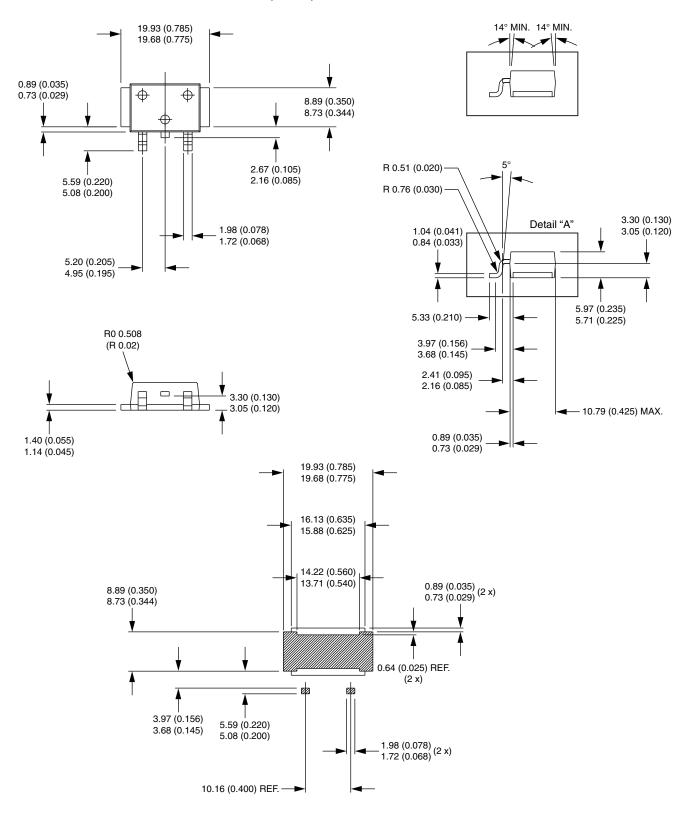
DIMENSIONS - D-61-8-SM in millimeters (inches)







DIMENSIONS - D-61-8-SL in millimeters (inches)





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