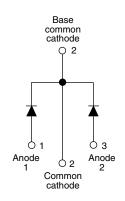
## VS-30CPQ1.0PbF Series, VS-30CPQ1.0-N3 Series

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## Schottky Rectifier, 2 x 15 A

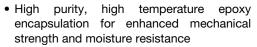


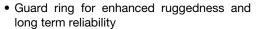


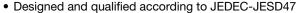
PRODUCT SUMMARY						
Package	TO-247AC					
I <sub>F(AV)</sub>	2 x 15 A					
$V_R$	140 V, 150 V					
V <sub>F</sub> at I <sub>F</sub>	0.78 V					
I <sub>RM</sub> max.	15 mA at 125 °C					
T <sub>J</sub> max.	175 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	11.25 mJ					

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- · High frequency operation







 Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>







#### **DESCRIPTION**

The VS-30CPQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform	30	A					
V <sub>RRM</sub>		150	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1000	A					
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.78	V					
TJ		- 55 to 175	°C					

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-30CPQ140PbF	VS-30CPQ140-N3	VS-30CPQ150PbF	VS-30CPQ150-N3	UNITS			
Maximum DC reverse voltage	$V_{R}$								
Maximum working peak reverse voltage	$V_{RWM}$	140	140	150	150	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS			
Maximum average per device		50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		30				
forward current See fig. 5 per leg	I <sub>F(AV)</sub>			15	٨			
Maximum peak one cycle non-repetitive surge current per leg	l=a	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000	A			
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	340				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 90 mH		11.25	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		0.50	Α			



# VS-30CPQ1.0PbF Series, VS-30CPQ1.0-N3 Series

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop per leg See fig. 1		15 A	T <sub>.1</sub> = 25 °C	1.00	V			
	V <sub>FM</sub> <sup>(1)</sup>	30 A	1j=25 C	1.19				
		15 A	T <sub>.1</sub> = 125 °C	0.78				
		30 A	1j = 125 C	0.93				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	0.1	· mA			
See fig. 2	'RM (*)	T <sub>J</sub> = 125 °C	VR = nateu VR	15				
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		340	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.5	nΗ			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs			

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width  $<300~\mu s,$  duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and stora temperature range	ge	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg		$R_{thJC}$	DC operation See fig. 4	2.20				
Maximum thermal resistance, junction to case per package		□thJC	DC operation 1.10		°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24				
Approximate weight				6	g			
Approximate weight				0.21	OZ.			
May water a target a				6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
Marking device			Coop obula TO 247AC (JEDEC)	30CPQ140				
			Case style TO-247AC (JEDEC)	30CPQ150				

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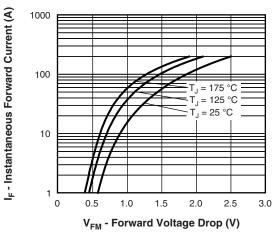


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

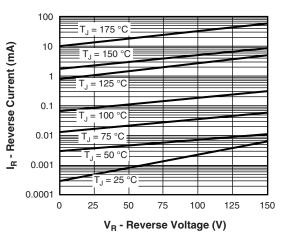


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

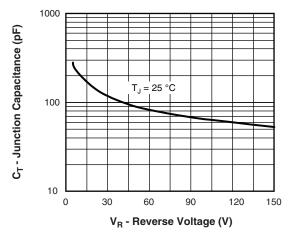


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

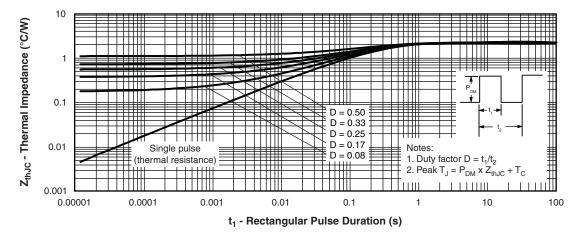


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics (Per Leg)

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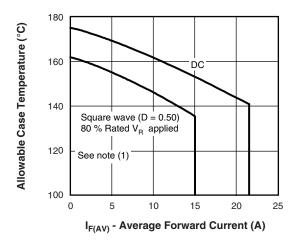


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

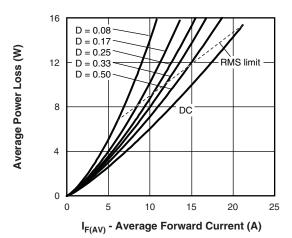


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

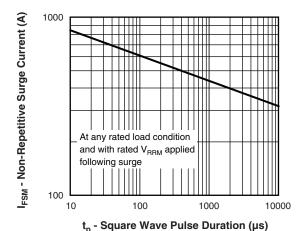


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

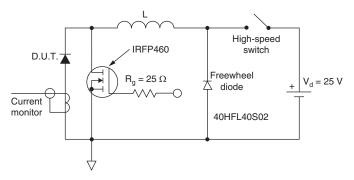


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

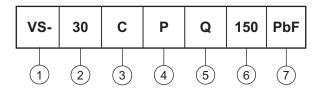
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

## VS-30CPQ1.0PbF Series, VS-30CPQ1.0-N3 Series

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#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 = 30 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

140 = 140 V

6 - Voltage code

150 = 150 V

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPT									
VS-30CPQ140PbF	25	500	Antistatic plastic tube						
VS-30CPQ140-N3	25	500	Antistatic plastic tube						
VS-30CPQ150PbF	25	500	Antistatic plastic tube						
VS-30CPQ150-N3	25	500	Antistatic plastic tube						

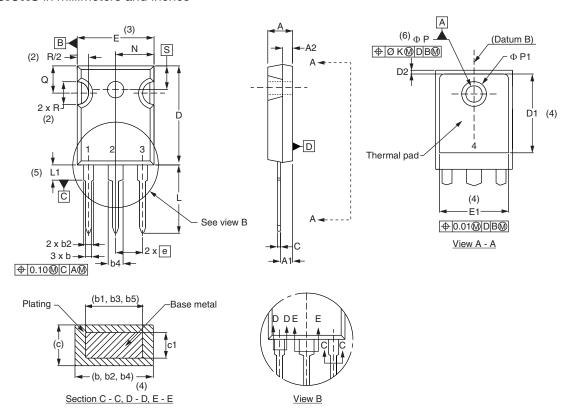
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95542</u>							
Part marking information	TO-247ACPbF	www.vishay.com/doc?95226					
	TO-247AC-N3	www.vishay.com/doc?95007					



Vishay Semiconductors

### TO-247 - 50 mils L/F

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES S	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	'BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}$  Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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