

High Performance Schottky Rectifier, 240 A



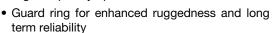


HALF-PAK	(D-67)
	, ,

PRODUCT SUMMARY			
I _{F(AV)}	240 A		
V_{R}	15 V		
Package	HALF-PAK (D-67)		
Circuit	Single diode		

FEATURES

- 125 °C T_J operation
- Low forward voltage drop
- High frequency operation



- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-245NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	240	А		
V _{RRM}		15			
I _{FSM}	t _p = 5 µs sine	s sine 20 000			
V _F	240 A _{pk} , T _J = 75 °C	240 A _{pk} , T _J = 75 °C 0.37			
T _J	Range	-55 to +125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-245NQ015PbF	UNITS		
Maximum DC reverse voltage	V _R	15	V		
Maximum working peak reverse voltage	V_{RWM}	25	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 73 °C, rec	ctangular waveform	240	
Maximum peak one cycle non-repetitive surge current I _{FSM}	l=	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	20 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	3000	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 5 A, L = 1 mH		12	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		А	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VA		VALUES	UNITS
		240 A	T _J = 25 °C	0.52	V
Maximum forward voltage drop	V (1)	480 A		0.61	
See fig. 1	V _{FM} ⁽¹⁾	240 A	T _J = 125 °C	0.37	
		480 A		045	
Maximum reverse leakage current	erse leakage current	T _J = 25 °C	V _B = Rated V _B	80	mA
See fig. 2	I _{RM} (1)	T _J = 125 °C	v _R = nateu v _R	4000	IIIA
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		15 800	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane 5.0		5.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µ		V/µs	

Note

 $^{^{(1)}}$ Pulse width $< 500 \ \mu s$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	1	T _J , T _{Stg}		-55 to 125	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	C/VV	
Approximate weight				30	g	
Approximate weight				1.06	oz.	
Mounting toyana	minimum			3 (26.5)		
Mounting torque	maximum		No. 1 heiseled thought	4 (35.4)	N · m (lbf · in)	
Tamainal kawana	minimum		Non-lubricated threads	3.4 (30)		
Terminal torque	maximum			5 (44.2)		
Case style				HALF-PA	K module	

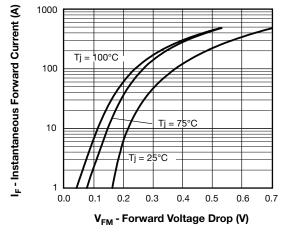


Fig. 1 - Maximum Forward Voltage Drop Characteristics

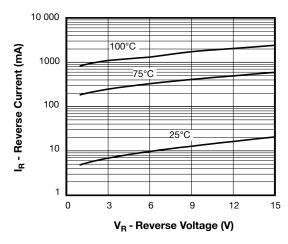


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



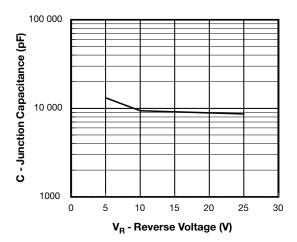


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

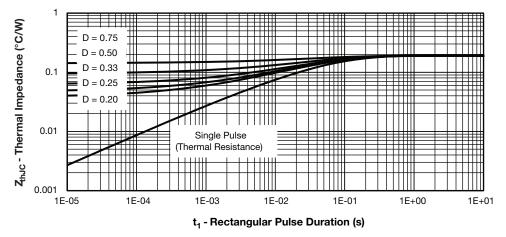


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

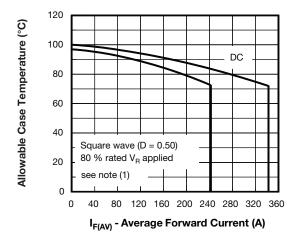


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

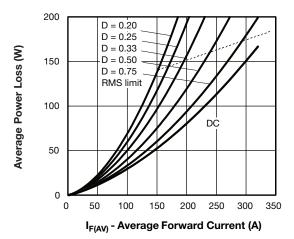
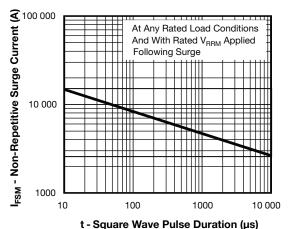


Fig. 6 - Forward Power Loss Characteristics



t - Square wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

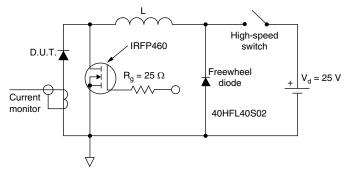


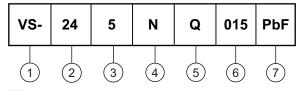
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



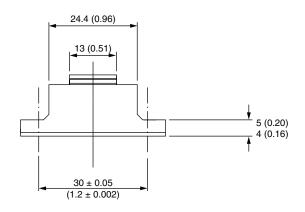
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- 3 Product silicon identification
- **4** N = Not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (015 = 15 V)
- Lead (Pb)-free

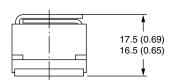
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95020		

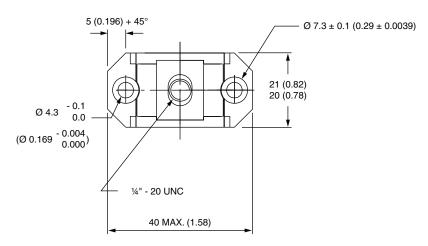


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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