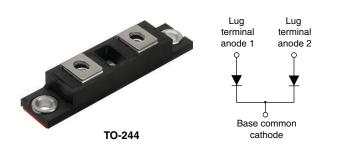
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High Performance Schottky Rectifier, 200 A



PRODUCT SUMMARY			
I _{F(AV)}	200 A		
V _R	100 V		
Package	TO-244		
Circuit	Two diodes common cathodes		

FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-203CNQ.. center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	200	А			
V _{RRM}		100	V			
I _{FSM}	t _p = 5 μs sine	12 800	А			
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.70	V			
TJ	Range	- 55 to 175	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-203CNQ100PbF	UNITS	
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V _{RWM}	100	v	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	L TEST CONDITIONS		VALUES	UNITS	
Maximum average	per leg		50 % duty cycle at T_{C} = 142 °C, rectangular waveform			100	٨
forward current See fig. 5	per device	I _{F(AV)}			200	A	
Maximum peak one cycle non-repetitive surge current per leg			5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	12 800	А	
See fig. 7	rent per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	1700	~	
Non-repetitive avalanch	e energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 13 A, L = 0.2 mH		15	mJ	
Repetitive avalanche cu	irrent per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1	А	

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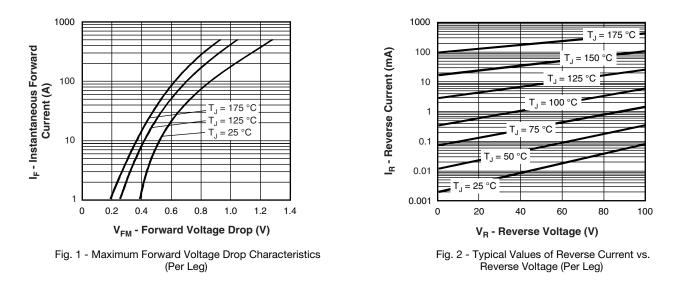
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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	100 A	T _{.1} = 25 °C	0.86	V
Maximum forward voltage drop per leg		200 A	1j=25 0	1.03	
See fig. 1		100 A	T 405.00	0.70	
		200 A	T _J = 125 °C	0.84	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3	mA
See fig. 2		T _J = 125 °C	V _R = naleu V _R	40	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.50	V
Forward slope resistance	r _t			1.08	mΩ
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2650	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs	

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	175	°C
Thermal resistance, junction to case	per leg	Build	-	-	0.38	°C/W
	per module	R _{thJC}	-	-	0.19	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
\\/_:-!++				68		g
Weight			-	2.4	-	oz.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	()
Vertical pull 2" lever pull			-	-	80	lbf ⋅ in
			-	-	35	חויוטו



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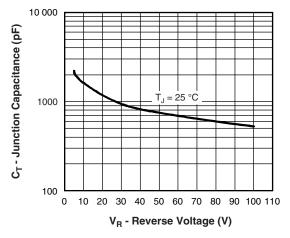


Fig. 3 - Typical Junction Capacitance vs.Reverse Voltage (Per Leg)

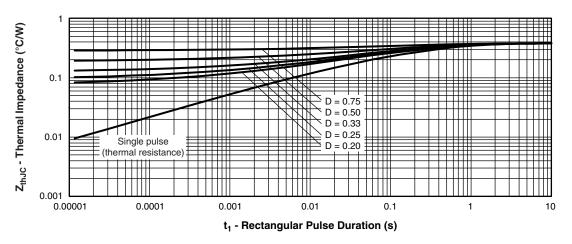


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

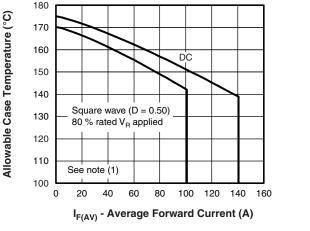


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

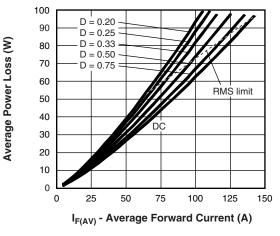


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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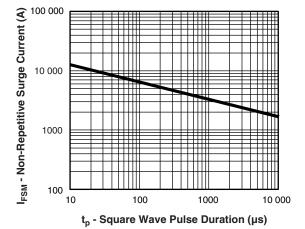


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

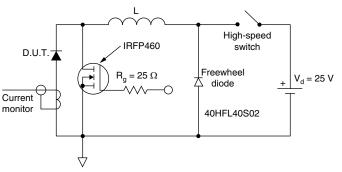
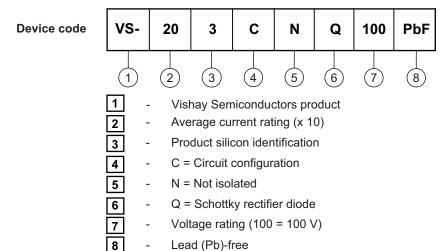


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = 80 \ \% \mbox{ rated } \mbox{V}_{R} \end{array}$

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95021</u>				
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⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

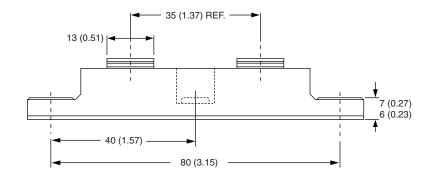


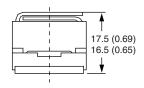


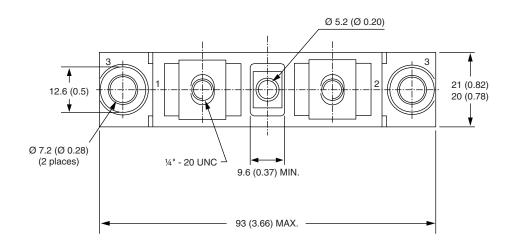
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TO-244

DIMENSIONS in millimeters (inches)









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