**Vishay Semiconductors** 

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High Performance Schottky Rectifier, 175 A



PowerTab<sup>®</sup>

PRODUCT SUMMARY			
Package	PowerTab <sup>®</sup>		
I <sub>F(AV)</sub>	175 A		
V <sub>R</sub>	30 V		
V <sub>F</sub> at I <sub>F</sub>	0.52 V		
I <sub>RM</sub>	650 mA at 125 °C		
T <sub>J</sub> max.	125 °C		
Diode variation	Single die		
E <sub>AS</sub>	80 mJ		

## FEATURES

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
  COMPLIANT
- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-175BGQ030 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
1	Rectangular waveform	175	А	
I <sub>F(AV)</sub>	T <sub>C</sub>	112	°C	
V <sub>RRM</sub>		30	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	7400	А	
M	175 A <sub>pk</sub> (typical)	0.47	V	
V <sub>F</sub>	TJ	150	°C	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-175BGQ030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>		V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 112 °C, rectangular waveform 175		А	
Maximum peak one cycle non-repetitive surge current	<b>1</b>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	7400	А
	10 ms sine or 6 ms rect. pulse		1400	~	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_{\rm J} = 25 \ {}^{\circ}{\rm C}, \ I_{\rm AS} = 12 \ {\rm A}, \ {\rm L} = 1.12 \ {\rm mH}$ 80 m		mJ	
Repetitive avalanche current	I <sub>AR</sub>	$\begin{tabular}{ c c c c } \hline Current decaying linearly to zero in 1 \mbox{$\mu$s$} \\ \hline Frequency limited by $T_J$ maximum $V_A$ = 1.5 x $V_R$ typical $$12$ A $$$		A	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>J</sub> = 25 °C	0.47	0.49	- V
Forward valtage drep		175 A		0.55	0.59	
Forward voltage drop		100 A	T <sub>J</sub> = 150 °C	0.36	0.39	
		175 A		0.47	0.52	
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 15 V		160	220	mA
Poverse leakage ourrent		T <sub>J</sub> = 150 °C, V <sub>R</sub> = 30 V		1400	2000	
Reverse leakage current		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1.3	4.5	
		T <sub>J</sub> = 125 °C		450	650	
Maximum junction capacitance	C <sub>T</sub>	$V_{\rm R}$ = 5 $V_{\rm DC}$ , (test signal range 100 kHz to 1 MHz), 25 $^\circ \rm C$		85	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		Rated V <sub>R</sub> 10 000		V/µs

#### Note

<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resis junction to case	Bth IC		DC operation	0.25	°C/W
Typical thermal resistar case to heatsink	B <sub>thos</sub> Mounting surface, smooth and greased 0.20		0.20	C/W	
Approximate weight				5	g
				0.18	oz.
	minimum			1.2 (10)	N·m
Mounting torque	maximum			2.4 (20)	(lbf $\cdot$ in)
Marking device Case style PowerTab®		175BC	Q045		

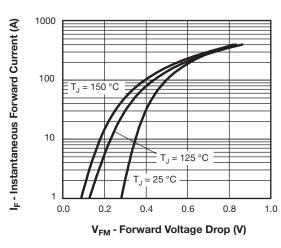
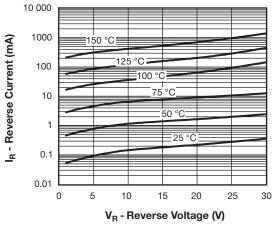
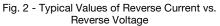


Fig. 1 - Maximum Forward Voltage Drop Characteristics





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## VS-175BGQ030

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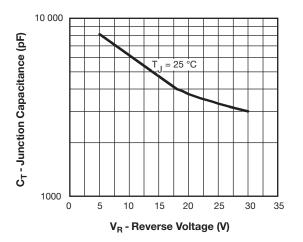
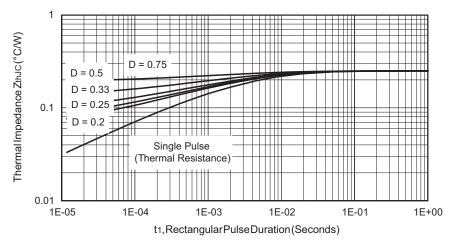
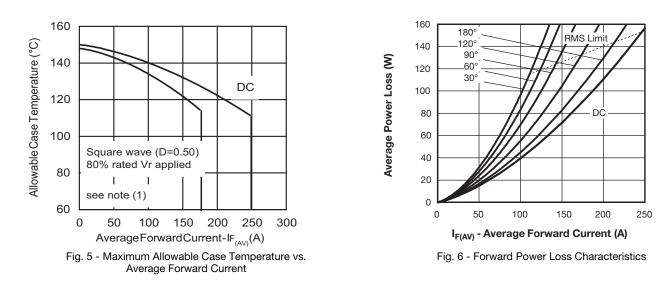


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage







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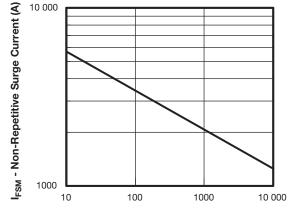
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t - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

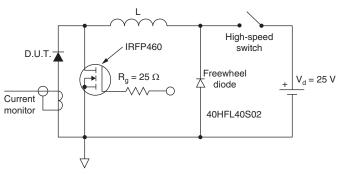


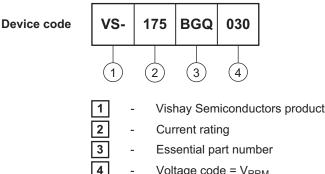
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

### **ORDERING INFORMATION TABLE**



Voltage code = V<sub>RRM</sub>

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95370			
SPICE model	www.vishay.com/doc?95427			
Application note	www.vishay.com/doc?95179			

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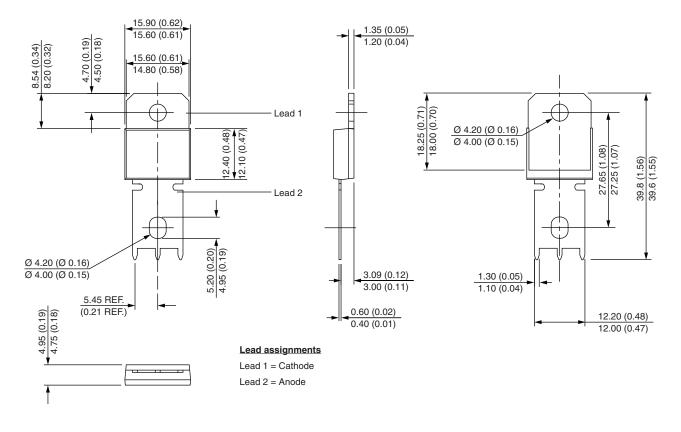
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### **DIMENSIONS** in millimeters (inches)





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