High Performance Schottky Rectifier, 1.5 A



PRODUCT SUMMARY				
Package	DO-214AC (SMA)			
I _{F(AV)}	1.5 A			
V _R	40 V			
V _F at I _F	0.43 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	6.0 mJ			

FEATURES

- Surface mountable
- Extremely low forward voltage



- Improved reverse blocking voltage capability relative to other similar size Schottky
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Switching power supplies
- Meter protection
- Reverse protection for power input to PC board circuits
- · Battery isolation and charging
- · Low threshold voltage diode
- Freewheeling or by-pass diode
- · Low voltage clamp

DESCRIPTION

The VS-15MQ040NPbF Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.5	Α	
V _{RRM}		40	V	
I _{FSM}	$t_p = 5 \mu s sine$	330	Α	
V _F	2 A _{pk} , T _J = 125 °C	0.43	V	
T _J	Range	-40 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-15MQ040NPbF	UNITS
Maximum DC reverse voltage	V_R	40	V
Maximum working peak reverse voltage	V_{RWM}	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 105 °C, reconstruction PC board 9 mm ² island (0.013)	ctangular waveform mm thick copper pad area)	2.1	А
See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 114 °C, red On PC board 9 mm ² island (0.013		1.5	
Maximum peak one cycle	_	5 μs sine or 3 μs rect. pulse	Following any rated load	330	
non-repetitive surge current See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	140	A
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 1.0		А	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	T 05 °C	0.42	V
Maximum forward voltage drop		2 A	- T _J = 25 °C	0.49	
See fig. 1	VFM ('')	1 A	T _ 105 °C	0.34	V
		2 A	T _J = 125 °C	0.43	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Pated V	0.5	mA
See fig. 2	IRM ('')	$V_R = Rated V_R$	v _R = nateu v _R	20	IIIA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.26	V
Forward slope resistance	r _t			64.6	mΩ
Typical junction capacitance	C _T	V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz		134	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0 n		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	Х	F

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

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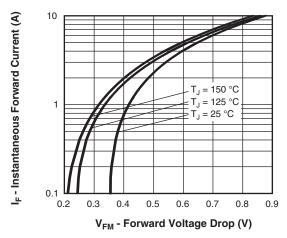


Fig. 1 - Maximum Forward Voltage Drop Characteristics

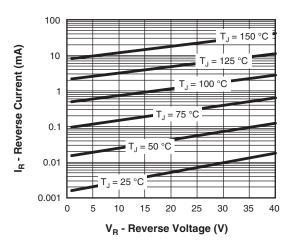


Fig. 2 - Typical Peak Reverse Current vs.Reverse Voltage

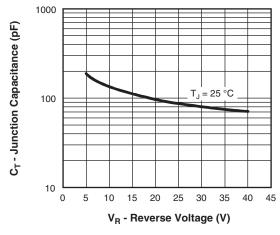


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

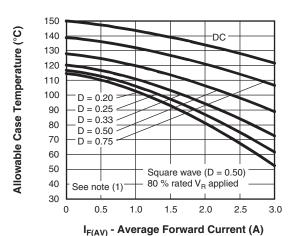


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

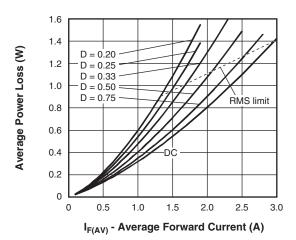


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

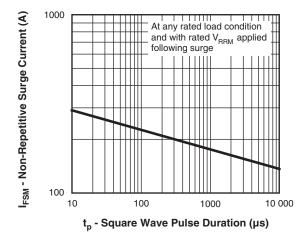


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

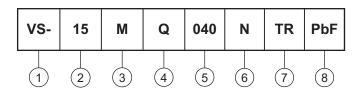
Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors products

2 - Current rating (15 = 1.5 A)

3 - M = SMA

4 - Q = Schottky "Q" series

Voltage rating (040 = 40 V)

6 - N = new SMA

7 - TR = tape and reel

8 - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-15MQ040NTRPbF	5AT	7500	13" diameter plastic tape and reel			

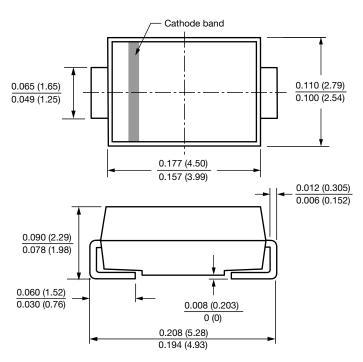
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95273			



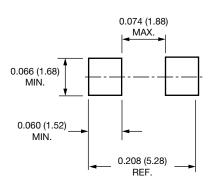
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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