VFT1045CBP

Vishay General Semiconductor

Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.34$ V at $I_F = 2.5$ A



PIN 3 O-	

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5.0 A			
V _{RRM}	45 V			
I _{FSM}	100 A			
V _F at I _F = 5.0 A	0.41 V			
T _{OP} max. (AC mode)	150 °C			
T _J max. (DC forward current)	200 °C			
Package	ITO-220AB			
Diode variation	Dual common cathode			

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- T_{.1} 200 °C max. in solar bypass mode application
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VFT1045CBP	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device	– I _{F(AV)} ⁽¹⁾	10	A	
	per diode		5.0		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	100	А	
Isolation voltage from terminal to heatsink, t = 1 min		V _{AC}	1500	V	
Operating junction and storage temperature range (AC mode)		T _{OP} , T _{STG}	-40 to +150	°C	
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$		T _J ⁽²⁾	≤ 200	°C	

Notes

⁽¹⁾ With heatsink

⁽²⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	$I_{F} = 2.5 \text{ A}$	$T_{\Lambda} = 25 ^{\circ}\text{C}$	V _F ⁽¹⁾	0.44	-	V
	$I_{F} = 5.0 \text{ A}$			0.49	0.58	
	I _F = 2.5 A	T _A = 125 °C		0.34	-	
	$I_{F} = 5.0 \text{ A}$			0.41	0.50	
Reverse current per diode		T _A = 25 °C	I _R ⁽²⁾	-	500	μA
		T _A = 125 °C		5	15	mA

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$ Pulse test: Pulse width $\leq 40\mbox{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	SYMBOL VFT1045CBP		
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	6.5	°C/W	
	per device		5.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT1045CBP-M3/4W	1.75	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

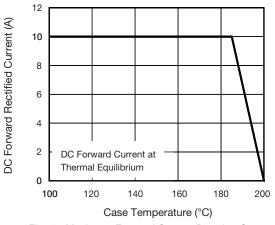


Fig. 1 - Maximum Forward Current Derating Curve

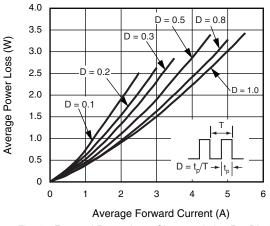
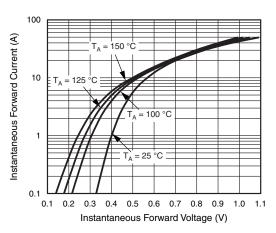


Fig. 2 - Forward Power Loss Characteristics Per Diode

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Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

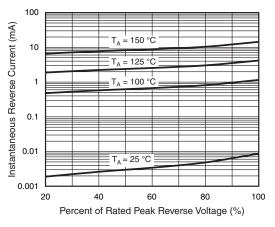


Fig. 4 - Typical Reverse Characteristics Per Diode

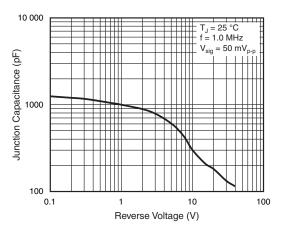


Fig. 5 - Typical Junction Capacitance Per Diode

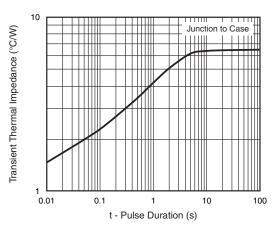
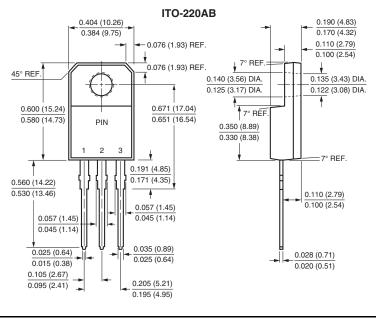


Fig. 6 - Typical Transient Thermal Impedance Per Diode



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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