HALOGEN FREE

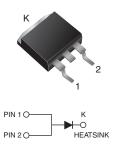


## Vishay General Semiconductor

# Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.30 \text{ V}$  at  $I_F = 5 \text{ A}$ 

#### TMBS<sup>®</sup> TO-263AB



PRIMARY CHARACTERISTCS				
Package	TO-263AB			
I <sub>F(DC)</sub>	30 A			
$V_{RRM}$	45 V			
I <sub>FSM</sub>	200 A			
V <sub>F</sub> at I <sub>F</sub> = 30 A	0.51 V			
T <sub>OP</sub> max. (AC mode)	150 °C			
T <sub>J</sub> max. (DC forward current)	200 °C			
Diode variation	Single die			

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

· High efficiency operation

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

- T<sub>J</sub> 200 °C max. in solar bypass application
- Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

#### **MECHANICAL DATA**

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VBT3045BP	UNIT	
Maximum repetitive peak reverse voltage	$V_{RRM}$	45	V	
Maximum DC forward bypassing current (fig. 1)	I <sub>F(DC)</sub> (1)	30	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200	А	
Operating junction temperature range (AC mode)	T <sub>OP</sub>	- 40 to + 150	°C	
Junction temperature in DC forward current without reverse bias, $t \le 1\ h$	T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C	

#### Notes

- (1) With heatsink
- (2) Meets the requirements of IEC 61215 ed.2 bypass diode thermal test



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.42	ı	- V
	I <sub>F</sub> = 15 A			0.49	-	
	I <sub>F</sub> = 30 A			0.58	0.70	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.30	-	
	I <sub>F</sub> = 15 A			0.40	-	
	I <sub>F</sub> = 30 A			0.51	0.60	
Reverse current	V <sub>R</sub> = 45 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	2000	μΑ
	v <sub>R</sub> = 45 v	T <sub>A</sub> = 125 °C		19	60	mA

#### **Notes**

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL VBT3045BP			
Typical thermal resistance	$R_{ heta JC}$	1.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT3045BP-M3/4W	1.37	4W	50/tube	Tube	
TO-263AB	VBT3045BP-M3/8W	1.37	8W	800/reel	Tape and reel	

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

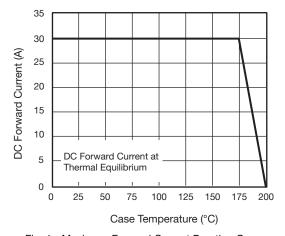


Fig. 1 - Maximum Forward Current Derating Curve

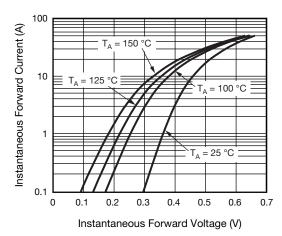
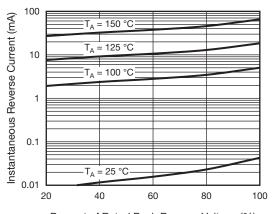


Fig. 2 - Typical Instantaneous Forward Characteristics



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Percent of Rated Peak Reverse Voltage (%) Fig. 3 - Typical Reverse Characteristics

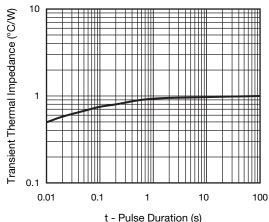


Fig. 5 - Typical Transient Thermal Impedance

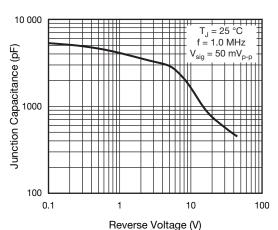
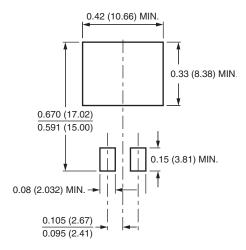


Fig. 4 - Typical Junction Capacitance

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### **TO-263AB** 0.411 (10.45) 0.190 (4.83) 0.380 (9.65) 0.160 (4.06) 0.055 (1.40) 0.245 (6.22) 0.045 (1.14) MIN. 0.055 (1.40) 0.360 (9.14) 0.047 (1.19) 0.320 (8.13) 0.624 (15.85) 0.591 (15.00) -0 to 0.01 (0 to 0.254) **→** 0.110 (2.79) 0.090 (2.29) 0.037 (0.940) 0.021 (0.53) 0.027 (0.686) 0.014 (0.36) 0.105 (2.67) 0.140 (3.56) 0.095 (2.41) 0.205 (5.20) 0.110 (2.79) 0.195 (4.95)

#### **Mounting Pad Layout**





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