Vishay General Semiconductor

Dual High Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.46$ V at $I_F = 5$ A



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V40DM120C PIN 1 C HEATSINK PIN 2 C

PRIMARY CHARACTERISTICS			
I _{F(AV)}	2 x 20 A		
V _{RRM}	120 V		
I _{FSM}	250 A		
V_F at I_F = 20 A (T_A = 125 °C)	0.64 V		
T _J max.	175 °C		
Package	TO-263AC (SMPD)		
Diode variations	Common cathode		

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, inductrial, and automotive application.

MECHANICAL DATA

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test Polarity: As marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V40DM120C	UNIT
Maximum repetitive peak reverse voltage		V _{RRM}	120	V
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	40	А
	per diode		20	A
Peak forward surge current 10 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	А
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +175	°C



RoHS COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F (1)	0.54	-	V
	I _F = 10 A			0.64	-	
	I _F = 20 A			0.79	0.89	
	I _F = 5 A	T _A = 125 °C		0.46	-	
	I _F = 10 A			0.55	-	
	I _F = 20 A			0.64	0.72	
Reverse current per diode	V _R = 90 V	T _A = 25 °C	I _R (2)	9	-	μA
		T _A = 125 °C		4.3	-	mA
	V _R = 120 V	T _A = 25 °C		-	500	μA
		T _A = 125 °C		7	32	mA

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40DM120C	UNIT	
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	2.0		
	per device		1.1	°C/W	
	per device	R _{0JA} (1)(2)	45		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V40DM120C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V40DM120CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

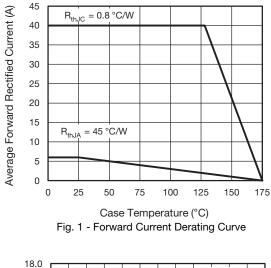
Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)



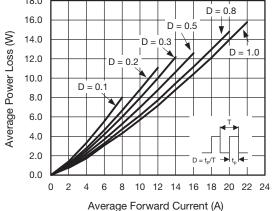
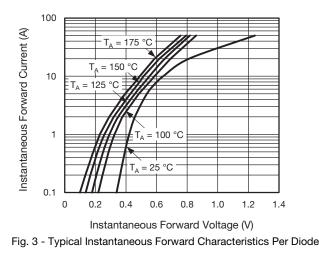
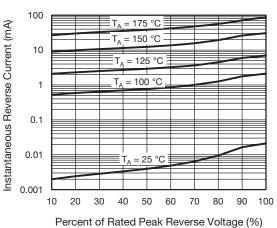
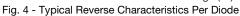
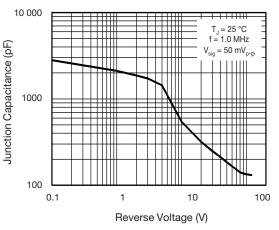


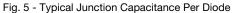
Fig. 2 - Forward Power Loss Characteristics Per Diode











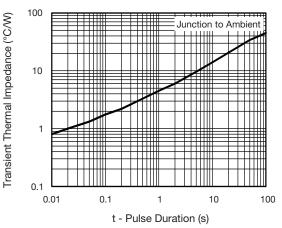


Fig. 6 - Typical Transient Thermal Impedance Per Device

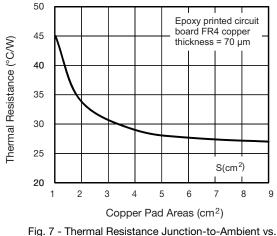
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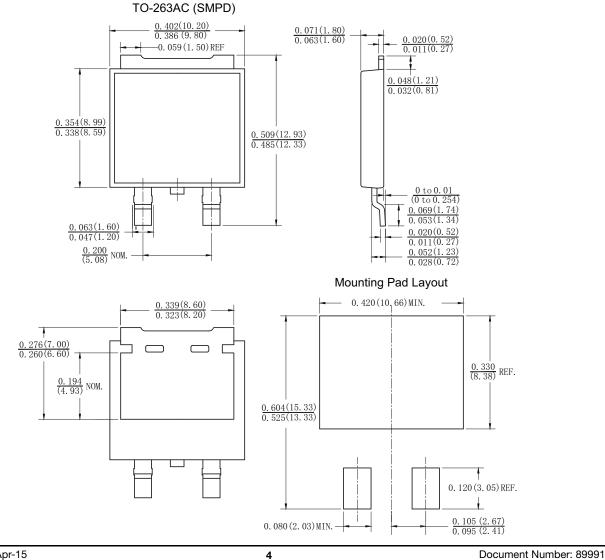


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Copper Pad Areas

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



 Revision: 08-Apr-15
 4
 Document Number: 899

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