- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation

Dual High-Voltage Trench MOS Barrier Schottky Rectifier Ultra Low $V_F = 0.42$ V at $I_F = 5$ A

- Solder dip 275 °C max. 10 s, per JESD 22-B106
 FREE
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

MECHANICAL DATA

Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40100PGW	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	per device		40	A	
	per diode	I _{F(AV)}	20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	A	
Non-repetitive avalanche energy at $T_J = 25$ °C, L = 70 r	E _{AS}	250	mJ		
Peak repetitive reverse current at $t_p = 2 \ \mu s$, 1 kHz, T _J = 38 °C ± 2 °C per diode		I _{RRM}	1.0	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	

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TMBS[®]

TO-3PW

PIN 1 0 PIN 2

PIN 3 0-CASE

2 x 20 A

100 V

250 A

250 mJ

0.67 V

150 °C

TO-3PW Dual common cathode

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

IFSM

E_{AS} at L = 70 mH

 V_F at $I_F = 20 A$

T_{.1} max.

Package

Diode variations

V40100PGW

ROHS COMPLIANT

Vishay General Semiconductor

www.vishay.com

Vishay General Semiconductor

V40100PGW

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	l _R = 1.0 mA	T _A = 25 °C	V _{BR}	100 (minimum)	-	V	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F (1)	0.49	-	V	
	I _F = 10 A			0.58	-		
	I _F = 20 A			0.76	0.85		
	I _F = 5 A	T _A = 125 °C		0.42	-		
	I _F = 10 A			0.54	-		
	I _F = 20 A			0.67	0.73		
Reverse current per diode	V _R = 70 V	T _A = 25 °C	I _R ⁽²⁾	16	-	μA	
		T _A = 125 °C		8.3	-	mA	
	V _R = 100 V	T _A = 25 °C		69	1000	μA	
		T _A = 125 °C		21	47	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	V40100PGW	UNIT		
Typical thermal resistance	per diode	- R _{θJC}	2.0	°C/W		
	per device		1.4	C/W		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-3PW	V40100PGW-M3/4W	4.5	4W	30/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

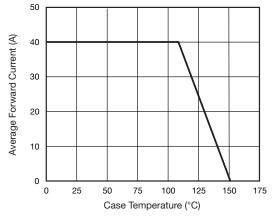


Fig. 1 - Forward Current Derating Curve

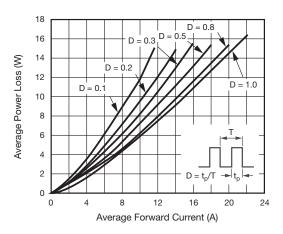


Fig. 2 - Forward Power Loss Characteristics Per Diode

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V40100PGW





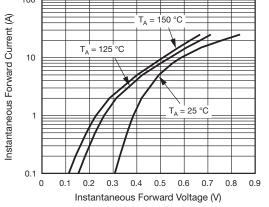


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

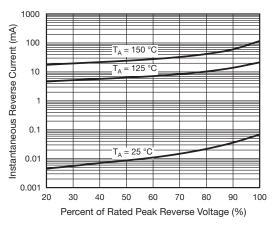


Fig. 4 - Typical Reverse Characteristics Per Diode

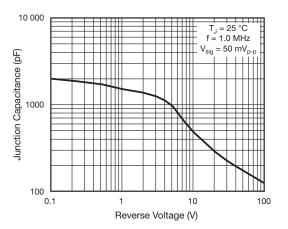


Fig. 5 - Typical Junction Capacitance Per Diode

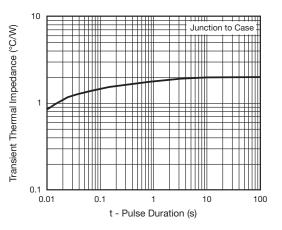
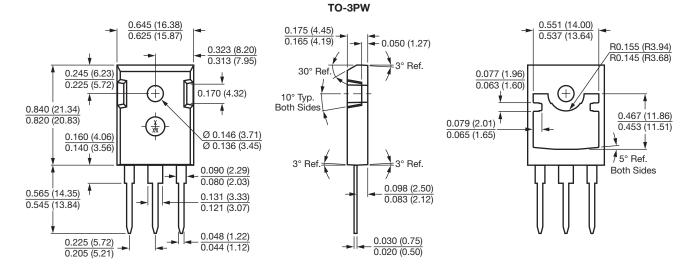


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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