COMPLIANT

HALOGEN FREE

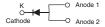


### Vishay General Semiconductor

# High Current Density Surface Mount Schottky Barrier Rectifier



#### **TO-277A (SMPC)**



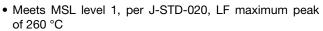
PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	10 A				
V <sub>RRM</sub>	50 V, 60 V				
I <sub>FSM</sub>	280 A				
E <sub>AS</sub>	20 mJ				
V <sub>F</sub> at I <sub>F</sub> = 10 A	0.55 V				
T <sub>J</sub> max.	150 °C				
Package	TO-277A (SMPC)				
Diode variations	Single				

#### **TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling diodes, DC/DC converters, and polarity protection application.

#### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Guardring for overvoltage protection
- · Low forward voltage drop, low power losses
- · High efficiency
- · Low thermal resistance



- AEC-Q101 qualified available
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant and AEC-Q101 gualified

Base P/NHM3\_X - halogen-free, RoHS-compliant and AEC-Q101 qualified  $\,$ 

("\_X" denotes revision code e.g. A, B,....)

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SS10P5	SS10P6	UNIT	
Device marking code		S105	S106		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	60	V	
Maximum average forward rectified current (fig. 1)		10 <sup>(1)</sup>		А	
	I <sub>F(AV)</sub>	7 (2)			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	280		А	
Non-repetitive avalanche energy at I <sub>AS</sub> = 2 A, T <sub>J</sub> = 25 °C	E <sub>AS</sub>	20		mJ	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150		°C	

#### **Notes**

- (1) Units mounted on infinite heatsink
- (2) Units mounted on 5 cm x 5 cm, 2 oz. copper pad



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.51	-	
	I <sub>F</sub> = 7 A			0.55	0.67	
	I <sub>F</sub> = 10 A			0.59		V
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.42	-	
	I <sub>F</sub> = 7 A			0.47	-	
	I <sub>F</sub> = 10A			0.55	0.63	
Reverse current	Pated V-	Rated $V_R$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	I <sub>R</sub> <sup>(2)</sup>	7.8	150	μΑ
	nateu v <sub>R</sub>			5.9	15	mA
Typical junction capacitance	4.0 V, 1 MHz	4.0 V, 1 MHz		560	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width  $\leq$  40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	SS10P5 SS10P6		UNIT		
Typical thermal resistance per diode	R <sub>0JA</sub> <sup>(1)</sup>	60		°C/W		
Typical thermal resistance per diode	$R_{ hetaJL}$	3		C/VV		

#### Note

<sup>(1)</sup> Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SS10P6-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel		
SS10P6-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel		
SS10P6HM3/86A (1)	0.10	86A	1500	7" diameter plastic tape and reel		
SS10P6HM3/87A <sup>(1)</sup>	0.10	87A	6500	13" diameter plastic tape and reel		
SS10P6HM3_A/H (1)	0.10	Н	1500	7" diameter plastic tape and reel		
SS10P6HM3_A/I (1)	0.10	I	6500	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified

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### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise specified)

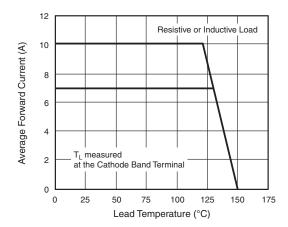


Fig. 1 - Maximum Forward Current Derating Curve

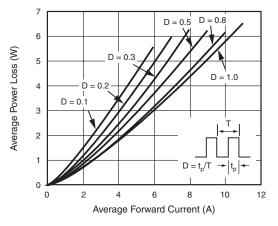


Fig. 2 - Forward Power Loss Characteristics

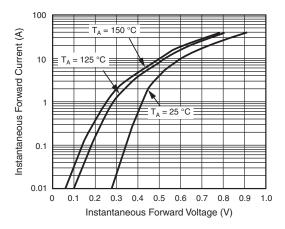


Fig. 3 - Typical Instantaneous Forward Characteristics

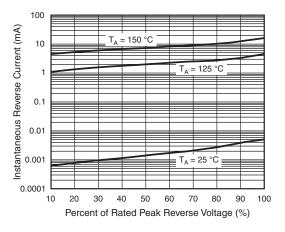


Fig. 4 - Typical Reverse Leakage Characteristics

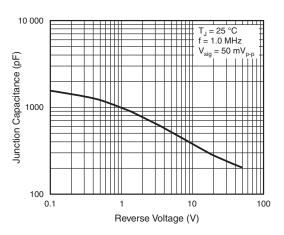


Fig. 5 - Typical Junction Capacitance

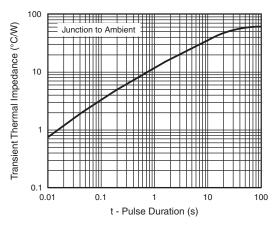
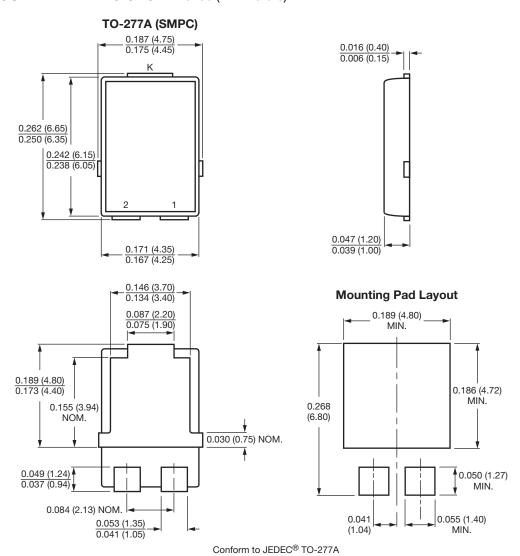


Fig. 6 - Typical Transient Thermal Impedance



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#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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