

Vishay General Semiconductor

Low V_F Surface Mount Schottky Rectifier



DO-214AC (SMA)

PRIMARY CHARACTERISTICS						
I _{F(AV)}	1.5 A					
V _{RRM}	20 V, 30 V					
I _{FSM}	50 A					
V _F	0.34 V					
T _J max.	125 °C					
Package	DO-214AC					
Diode variations	Single					

FEATURES

- Low profile package
- · Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- Very low forward voltage drop
- · High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 gualified
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3_X - RoHS-compliant and AEC-Q101 qualified ("_X" denotes revision code e.g. A, B,)

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SL12	SL13	UNIT		
Device marking code		SL2	SL3			
Maximum repetitive peak reverse voltage	V _{RRM}	20	30	V		
Maximum RMS voltage	V _{RMS}	14	21	V		
Maximum DC blocking voltage	V _{DC}	20 30		V		
Maximum average forward rectified current at $T_L = 105 \ ^{\circ}C$ (fig. 1)	I _{F(AV)}	1.5		A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50		A		
Voltage rate of change (rated V _R)	dV/dt	10 000		V/µs		
Operating junction temperature range	TJ	-55 to +125		°C		
Storage temperature range	T _{STG}	-55 to	°C			

RoHS COMPLIANT



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	SL12	SL13	UNIT	
Maximum instantaneous forward voltage at ⁽¹⁾	I _F = 0.1 A	T _A = 125 °C	V _F	0.230		V	
		T _A = 25 °C		0.360			
	I _F = 1.0 A	T _A = 125 °C		V _F 0.340 0.445			
		T _A = 25 °C					
Maximum DC reverse current	$T_{A} = 25 \text{ °C}$ $T_{A} = 100 \text{ °C}$	1	0.2		mA		
at rated DC blocking voltage ⁽¹⁾		T _A = 100 °C	IR	6	.0	IIIA	

Note

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	SL12	SL13	UNIT		
Maximum thermal resistance ⁽¹⁾	$R_{ ext{ heta}JA}$	88		°C/W		
	$R_{ ext{ heta}JL}$	28				

Note

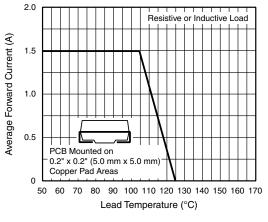
⁽¹⁾ PCB mounted on 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas

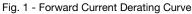
ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE BASE QUAN		DELIVERY MODE		
SL13-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel		
SL13-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel		
SL13HE3_A/H (1)	0.064	Н	1800	7" diameter plastic tape and reel		
SL13HE3_A/I (1)	0.064		7500	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)





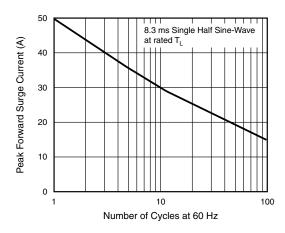
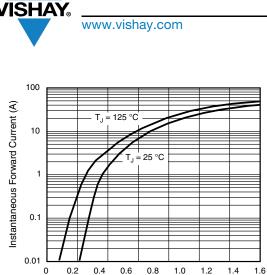


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current





Instantaneous Forward Voltage (V)

Fig. 3 - Typical Instantaneous Forward Characteristics

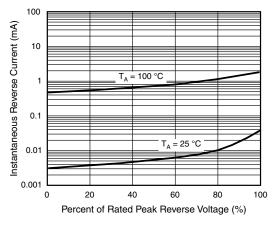
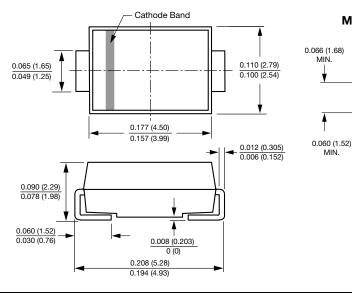


Fig. 4 - Typical Reverse Characteristics





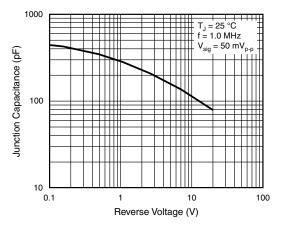


Fig. 5 - Typical Junction Capacitance

Mounting Pad Layout

0.208 (5.28) REF.

MIN.

_0.074 (1.88) MAX.





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