COMPLIANT

HALOGEN

**FREE** 





## N-Channel Reduced $Q_g$ , Fast Switching MOSFET

| PRODUCT SUMMARY     |                                   |                    |  |  |
|---------------------|-----------------------------------|--------------------|--|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$              | I <sub>D</sub> (A) |  |  |
| 30                  | 0.0032 at V <sub>GS</sub> = 10 V  | 25                 |  |  |
| 30                  | 0.0036 at V <sub>GS</sub> = 4.5 V | 22                 |  |  |

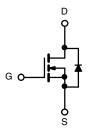
| (A) |  |
|-----|--|
| 5   |  |
| 2   |  |

### **FEATURES**

- Halogen-free According to IEC 61249-2-21 **Definition**
- Extremely Low Q<sub>gd</sub> for Switching Losses Improvement
- TrenchFET® Gen II Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

### **APPLICATIONS**

- Low-Side DC/DC Conversion
  - Notebook, Server, VRM Module
- Fixed Telecom



N-Channel MOSFET

|                          | SO-8     |                  |             |
|--------------------------|----------|------------------|-------------|
| S 1<br>S 2<br>S 3<br>G 4 |          | 8<br>7<br>6<br>5 | D<br>D<br>D |
|                          | Top View |                  |             |

Ordering Information: Si4368DY-T1-E3 (Lead (Pb)-free)

Si4368DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

| ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted) |                                   |                  |              |      |    |
|---|-----------------------------------|------------------|--------------|------|----|
| Parameter   | Symbol                            | 10 s             | Steady State | Unit |    |
| Drain-Source Voltage  |                                   | V <sub>DS</sub>  | 30           |      | V  |
| Gate-Source Voltage   |                                   | V <sub>GS</sub>  | ± 12         |      | V  |
| Continuous Drain Current (T <sub>.1</sub> = 150 °C) <sup>a</sup>          | T <sub>A</sub> = 25 °C            | - I <sub>D</sub> | 25           | 17   |    |
| Continuous Diain Current (1) = 150 C)                                     | T <sub>A</sub> = 70 °C            |                  | 20           | 13   |    |
| Pulsed Drain Current (10 μs Pulse Width)                                  |                                   | I <sub>DM</sub>  | 70           |      | Α  |
| Continuous Source Current (Diode Conduction) <sup>a</sup>                 |                                   | I <sub>S</sub>   | 2.9          | 1.3  |    |
| Avalanch Current  | L = 0.1 mH                        | I <sub>AS</sub>  | 50           |      |    |
| Maximum Power Dissipation <sup>a</sup>                                    | T <sub>A</sub> = 25 °C            | В                | 3.5 1.6      |      | W  |
| Maximum Fower Dissipation   | T <sub>A</sub> = 70 °C            | P <sub>D</sub>   | 2.2          | 1    | VV |
| Operating Junction and Storage Temperature Range                          | T <sub>J</sub> , T <sub>stg</sub> | - 55 t           | o 150        | °C   |    |

| THERMAL RESISTANCE RATINGS               |              |                   |         |         |      |  |
|--|--------------|-------------------|---------|---------|------|--|
| Parameter                                |              | Symbol            | Typical | Maximum | Unit |  |
| Maximum Junction-to-Ambient <sup>a</sup> | t ≤ 10 s     | В                 | 29      | 35      |      |  |
| Maximum Junction-to-Ambient              | Steady State | $R_{thJA}$        | 67      | 80      | °C/W |  |
| Maximum Junction-to-Foot (Drain)         | Steady State | R <sub>thJF</sub> | 13      | 16      | 1    |  |

a. Surface mounted on 1" x 1" FR4 board.

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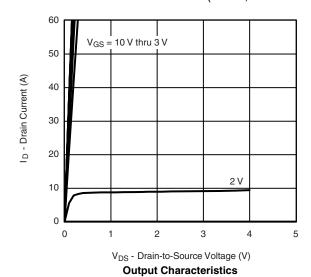
| Parameter                                     | Symbol              | ol Test Conditions   |     | Тур.   | Max.   | Unit |  |
|---|---------------------|--|-----|--------|--------|------|--|
| Static  |                     |  |     |        |        |      |  |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$   | 0.6 |        | 1.8    | V    |  |
| Gate-Body Leakage                             | I <sub>GSS</sub>    | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$  |     |        | ± 100  | nA   |  |
| Zero Gate Voltage Drain Current               | 1                   | V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V<br>V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C |     |        | 1      | μΑ   |  |
| Zero Gate voltage Drain Current               | I <sub>DSS</sub>    |  |     |        | 5      |      |  |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>  | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$  | 30  |        |        | Α    |  |
| Drain-Source On-State Resistance <sup>a</sup> |                     | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A  |     | 0.0026 | 0.0032 | - 0  |  |
| Drain-Source On-State Resistance              | R <sub>DS(on)</sub> | V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 22 A   |     | 0.0029 | 0.0036 |      |  |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>     | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 25 A  |     | 150    |        | S    |  |
| Diode Forward Voltage <sup>a</sup>            | $V_{SD}$            | I <sub>S</sub> = 2.9 A, V <sub>GS</sub> = 0 V  |     | 0.66   | 1.1    | V    |  |
| Dynamic <sup>b</sup>                          |                     |  |     |        |        |      |  |
| Input Capacitance                             | C <sub>iss</sub>    |  |     | 8340   |        |      |  |
| Output Capacitance                            | C <sub>oss</sub>    | $V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$   |     | 850    |        | pF   |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>    |  |     | 355    |        |      |  |
| Total Gate Charge                             | $Q_{g}$             |  |     | 53     | 80     |      |  |
| Gate-Source Charge                            | $Q_{gs}$            | $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$  |     | 17.5   |        | nC   |  |
| Gate-Drain Charge                             | $Q_{gd}$            |  |     | 6.5    |        |      |  |
| Gate Resistance                               | $R_{g}$             | f = 1 MHz  | 0.8 | 1.2    | 1.8    | Ω    |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>  |  |     | 25     | 38     |      |  |
| Rise Time                                     | t <sub>r</sub>      | $V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$   |     | 20     | 30     |      |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub> | $I_D \cong 1$ A, $V_{GEN} = 10$ V, $R_g = 6 \Omega$  |     | 172    | 260    | ns   |  |
| Fall Time                                     | t <sub>f</sub>      |  |     | 41     | 62     |      |  |
| Source-Drain Reverse Recovery Time            | t <sub>rr</sub>     | I <sub>F</sub> = 2.9 A, dl/dt = 100 A/μs   |     | 42     | 60     |      |  |

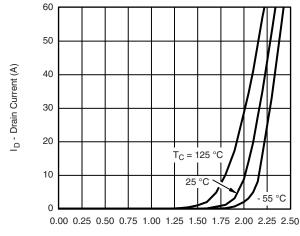
### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





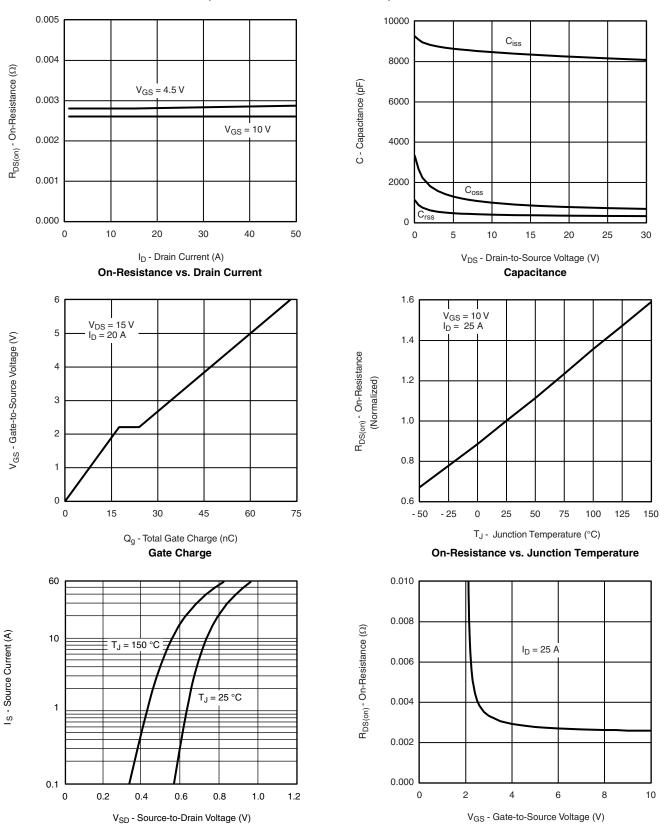
V<sub>GS</sub> - Gate-to-Source Voltage (V)

**Transfer Characteristics** 





### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



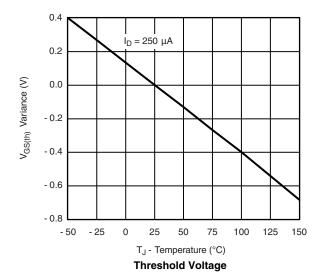
Source-Drain Diode Forward Voltage

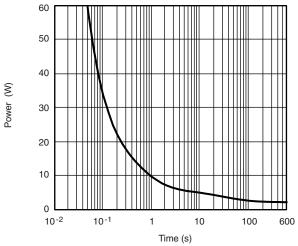
On-Resistance vs. Gate-to-Source Voltage

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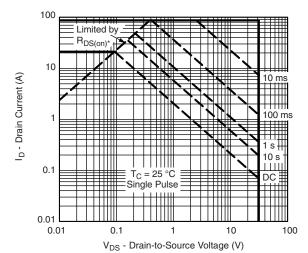
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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



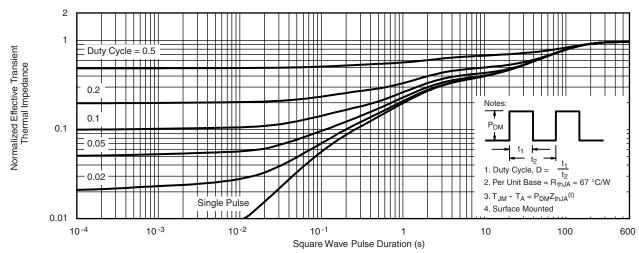


Single Pulse Power



 $^{\star}$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

### Safe Operating Area, Junction-to-Case

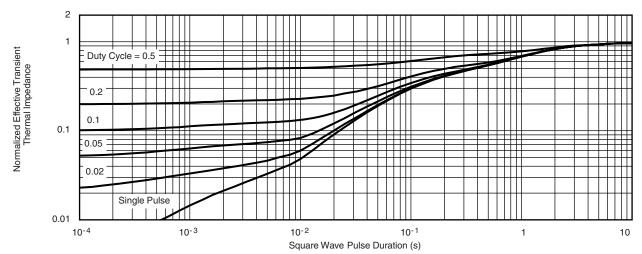


Normalized Thermal Transient Impedance, Junction-to-Ambient





### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?72704">www.vishay.com/ppg?72704</a>.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







|                                | MILLIM | IETERS | INCHES    |       |  |  |
|--------------------------------|--------|--------|-----------|-------|--|--|
| DIM                            | Min    | Max    | Min       | Max   |  |  |
| Α                              | 1.35   | 1.75   | 0.053     | 0.069 |  |  |
| A <sub>1</sub>                 | 0.10   | 0.20   | 0.004     | 0.008 |  |  |
| В                              | 0.35   | 0.51   | 0.014     | 0.020 |  |  |
| С                              | 0.19   | 0.25   | 0.0075    | 0.010 |  |  |
| D                              | 4.80   | 5.00   | 0.189     | 0.196 |  |  |
| Е                              | 3.80   | 4.00   | 0.150     | 0.157 |  |  |
| е                              | 1.27   | BSC    | 0.050 BSC |       |  |  |
| Н                              | 5.80   | 6.20   | 0.228     | 0.244 |  |  |
| h                              | 0.25   | 0.50   | 0.010     | 0.020 |  |  |
| L                              | 0.50   | 0.93   | 0.020     | 0.037 |  |  |
| q                              | 0°     | 8°     | 0°        | 8°    |  |  |
| S                              | 0.44   | 0.64   | 0.018     | 0.026 |  |  |
| ECN: C-06527-Rev. I. 11-Sep-06 |        |        |           |       |  |  |

DWG: 5498

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### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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