MBR40H35CT, MBR40H45CT, MBR40H50CT, MBR40H60CT

Vishay General Semiconductor

### **Dual Common Cathode Schottky Rectifiers**

High Barrier Technology for Improved High Temperature Performance

# TO-220AB PIN 2 CASE

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PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 20 A					
V <sub>RRM</sub>	35 V, 45 V, 50 V, 60 V					
I <sub>FSM</sub>	350 A, 320 A					
$V_F$ at $I_F$ = 20 A	0.55 V, 0.60 V					
I <sub>R</sub>	100 µA					
T <sub>J</sub> max.	175 °C					
Package	TO-220AB					
Diode variations	Dual Common Cathode					

### **FEATURES**

- Power pack
- Guardring for overvoltage protection
- · Lower power losses, high efficiency
- Low forward voltage drop
- Low leakage current
- · High forward surge capability
- High frequency operation
- Solder dip 275 °C max., 10 s, per JESD 22-B106
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, or polarity protection application.

### **MECHANICAL DATA**

#### Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

#### Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25$ °C unless otherwise noted)								
PARAMETER		SYMBOL	MBR40H35CT	MBR40H45CT	MBR40H50CT	MBR40H60CT	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	35	45	50	60	V	
Maximum average forward rectified current (fig. 1)	total device	1	40					
	per diode	I <sub>F(AV)</sub>	20					
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	350 320		20	А		
Peak repetitive reverse surge current per diode at $t_p = 2 \ \mu s$ , 1 kHz		I <sub>RRM</sub>	1.0			А		
Peak non-repetitive reverse surge energy (8/20 µs waveform) per diode		E <sub>RSM</sub>	20			mJ		
Non-repetitive avalanche energy at 25 °C, $I_{AS}$ = 3.0 A, L = 5 mH per diode		E <sub>AS</sub>	22.5				mJ	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000				V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 65 to + 175				°C	

RoHS

COMPLIANT

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)									
PARAMETER	SYMBOL	TEST CC	NDITIONS	MBR40H35CT	MBR40H45CT	MBR40H50CT	MBR40H60CT	UNIT	
Maximum instantaneous forward voltage per diode	V <sub>F</sub> <sup>(1)</sup>	I <sub>F</sub> = 20 A	T <sub>J</sub> = 25 °C	0.64		0.68			
		$I_F = 20 \text{ A}$	T <sub>J</sub> = 125 °C	0.55		0.60		v	
		$I_F = 40 \text{ A}$	T <sub>J</sub> = 25 °C	0.76		0.83			
		$I_{F} = 40 \text{ A}$	T <sub>J</sub> = 125 °C	0.70		0.	73		
Maximum instantaneous reverse current per diode	I <sub>R</sub> <sup>(2)</sup> ra	rated V <sub>R</sub> $\begin{array}{c} T_J = 25 \ ^{\circ}C \\ T_J = 125 \ ^{\circ}C \end{array}$		100			μA		
				15			mA		
Typical junction capacitance	CJ	4.0 V, 1 N	IHz per diode	1200 920		20	pF		

#### Notes

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 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	MBR40H35CT	MBR40H45CT	MBR40H50CT	MBR40H60CT	UNIT	
Thermal resistance, junction to case per diode	$R_{ ext{ heta}JC}$	1.8				°C/W	

ORDERING INFORMATION (Example)								
PACKAGE	PREFERRED P/N	RED P/N UNIT WEIGHT (g) PACKAGE CODE BASE QUANTITY DELIVERY						
TO-220AB	MBR40H45CT-E3/45	1.58	45	50/tube	Tube			

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

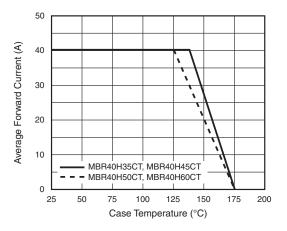
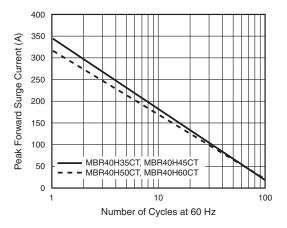
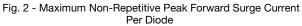


Fig. 1 - Forward Derating Curve Per Diode





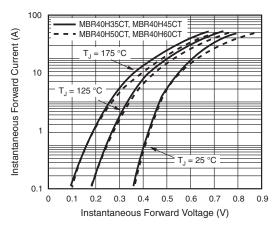
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Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

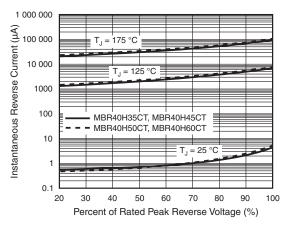


Fig. 4 - Typical Reverse Characteristics Per Diode

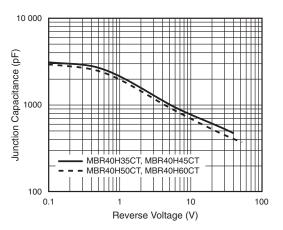


Fig. 5 - Typical Junction Capacitance Per Diode

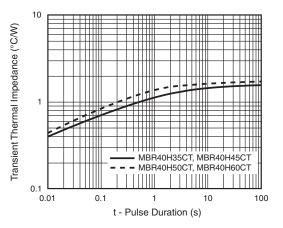
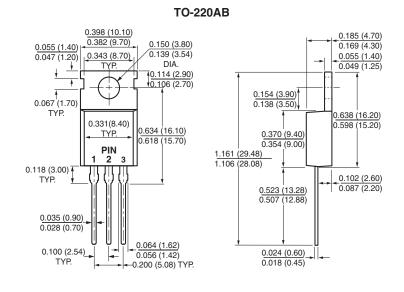


Fig. 6 - Typical Transient Thermal Impedance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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