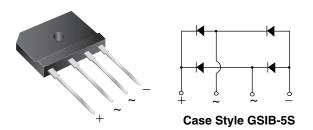


Vishay General Semiconductor

Low V_F Single-Phase Single In-Line Bridge Rectifiers



PRIMARY CHARACTERISTICS				
Package	GSIB-5S			
I _{F(AV)}	25 A			
V _{RRM}	600 V			
I _{FSM}	550 A			
I _R	10 µA			
V_F at I_F = 12.5 A, T_A = 125 °C	0.76 V			
T _J max.	150 °C			
Diode variations	In-Line			

FEATURES

- UL recognition file number E54214, Vol. 1
- Thin single in-line package
- Oxide planar chip junction
- Low forward voltage drop
- High surge current capability
- High case dielectric strength of 2500 V_{RMS}
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances and white-goods applications specially for telecom power supply, high efficiency desktop PC and server SMPS.

MECHANICAL DATA

Case: GSIB-5S Epoxy meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked on body

Mounting Torque: 10 cm-kg (8.8 in-lbs) maximum Recommended Torque: 5.7 cm-kg (5 in-lbs)

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	LVB2560	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	600	V	
Maximum average forward rectified output current at	T _C = 105 °C	I _O ⁽¹⁾	25	^	
	T _A = 25 °C	I _O ⁽²⁾	3.6	A	
Non-repetiitive peak forward surge current 8.3 ms single sine-wave, $T_J = 25$ °C		I _{FSM}	550	А	
Rating for fusing (t < 8.3 ms)	$T_J = 25 \ ^\circ C$	l ² t	1255	A ² s	
Operating junction and storage temperature range		T _J , T _{STG}	- 55 to + 150	°C	

Notes

⁽¹⁾ Unit case mounted on aluminum plate heatsink

⁽²⁾ Units mounted on PCB without heatsink

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COMPLIANT

HALOGEN

FREE

LVB2560



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 12.5 A	T _A = 25 °C	V _F ⁽¹⁾	0.89	0.92	V	
		T _A = 125 °C		0.76	-		
Reverse current per diode	V _R = 600 V	T _A = 25 °C	I _R ⁽²⁾	0.2	10	μA	
		T _A = 125 °C		140	-		
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	1.8	-	μs	
Typical junction capacitance	4.0 V, 1 MHz		CJ	330	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER SYMBOL LVB2560		LVB2560	UNIT	
Maximum thermal resistance	R _{0JA} ⁽²⁾	25	°C/W	
	R _{0JC} ⁽¹⁾	1.0	0/11	

Notes

⁽¹⁾ With heatsink

⁽²⁾ Without heatsink, free air

EMC SURGE IMMUNITY TEST STANDARD ($T_A = 25 \text{ °C}$, unless otherwise noted)						
STANDARD	TEST TYPE	TEST CONDITIONS SY		CLASS	VALUE	
IEC 61000-4-5	Power supply coupling mode, line to line	1.2/50 μs waveform, R = 2 $\Omega,$ T_A = 25 °C $^{(1)}$	V _{PEAK}	-	6 kV maximum	

Note

(1) Immunity to IEC 61000-4-5 peak pulse voltage test, 1.2/50 µs, 2 Ω, 5 times each of positive and negative polarity test

ORDERING INFORMATION (Example)							
PREFERRED P/N	P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY						
LVB2560-M3/45	7.1	45	20	Tube			

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

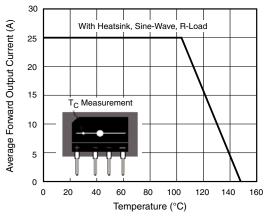


Fig. 1 - Derating Curve Output Rectified Current

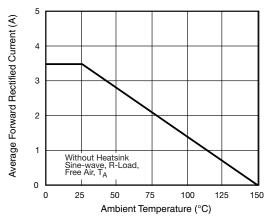


Fig. 2 - Forward Current Derating Curve

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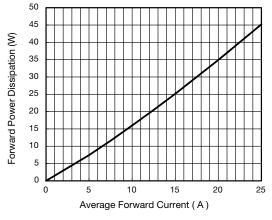
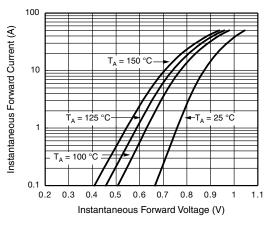
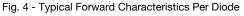


Fig. 3 - Forward Power Dissipation





PACKAGE OUTLINE DIMENSIONS in millimeters

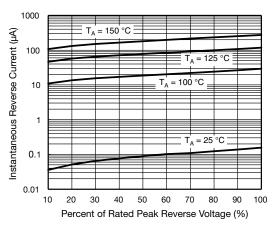
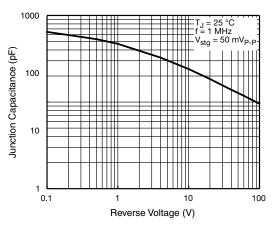
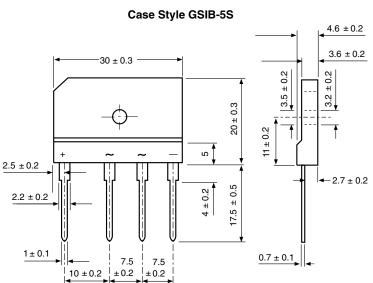


Fig. 5 - Typical Reverse Characteristics Per Diode







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