

Vishay Semiconductors

RoHS

HALOGEN

FREE

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°C

Schottky Rectifier, 2 x 20 A

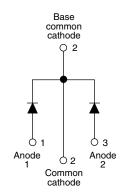


TO-247AC

 V_{RRM}

I_{FSM}

 T_{J}



PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 20 A						
V _R	45 V						
V _F at I _F	0.56 V						
I _{RM} max.	85 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	20 mJ						

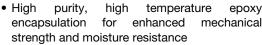
 $t_p = 5 \mu s sine$

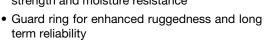
Range

20 Apk, T_J = 125 °C

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



The VS-MBR4045WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

45 1020

0.56

- 55 to 150

Diode variation	Common cathode 20 mJ	, , , , , , , , , , , , , , , , , , ,	oplications are in swite, freewheeling diodes,	5 1 11
MAJOR RATINGS AI	ID CHARACTERISTICS			
SYMBOL	CHARACTERISTICS		VALUES	UNITS
I _{F(AV)}	Rectangular waveform (per devic	e)	40	A
I _{FRM}	T _C = 125 °C (per leg)		A	

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-MBR4045WTPbF	VS-MBR4045WT-N3	UNITS				
Maximum DC reverse voltage	V _R	4E	45	V				
Maximum working peak reverse voltage	V_{RWM}	45	45	V				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average per		T _C = 125 °C, 50 % duty cycle, rectangular waveform		20					
forward current per dev	ice I _{F(AV)}			40					
Peak repetitive forward current per leg	I _{FRM}	Rated V_R , square wave, 20 kHz, T_C = 125 °C		40	Α				
Maximum peak one cycle non-repetitive surge current per leg	leo, ,	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1020					
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	265					
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.40 mH		20	mJ				
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	Α				

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VS-MBR4045WTPbF, VS-MBR4045WT-N3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		20 A	T _{.1} = 25 °C	0.59	V			
Maximum forward valtage drap	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.78				
Maximum forward voltage drop	VFM (1)	20 A	T _ 105 °C	0.56				
		40 A	T _J = 125 °C	0.72				
		T _J = 25 °C		1.75				
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 100 °C	Rated DC voltage	50	mA			
		T _J = 125 °C		85				
Threshold voltage	V _{F(TO)}	T. – T. movimum		0.29	V			
Forward slope resistance	r _t	ıj = ıjınaxımum	$T_J = T_J$ maximum		mΩ			
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	900	pF				
Typical series inductance	L _S	Measured from top of term	7.5	nΗ				
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs			

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	T_J		- 55 to 150	°C				
Maximum storage temperature range	T _{Stg}		- 55 to 175	.0				
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	1.4	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.7					
Approximate weight			6	g				
Approximate weight			0.21	OZ.				
Mounting torque minimum			6 (5)	kgf · cm				
maximum			12 (10)	(lbf \cdot in)				
Device marking		Case style TO-247AC (JEDEC)	MBR40	045WT				



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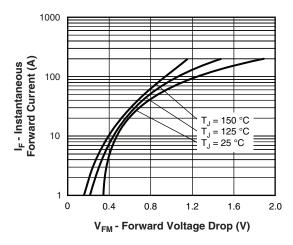


Fig. 1 - Maximum Forward Voltage Drop Characteristics

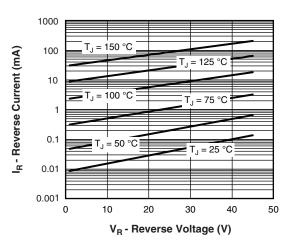


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

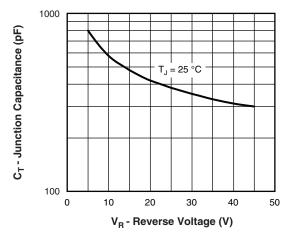


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

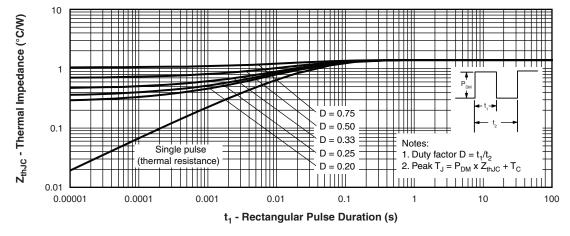


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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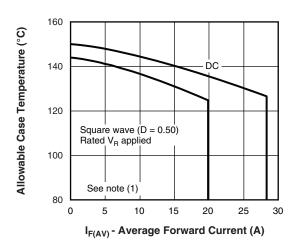


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

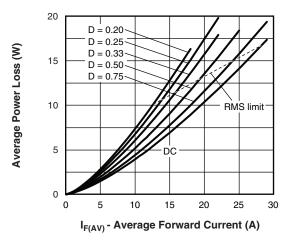


Fig. 6 - Forward Power Loss Characteristics

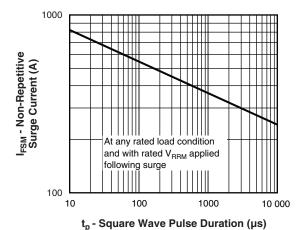


Fig. 7 - Maximum Non-Repetitive Surge Current

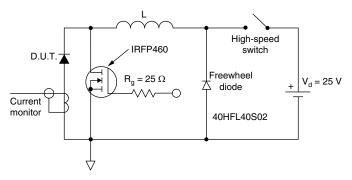


Fig. 8 - Unclamped Inductive Test Circuit

Note

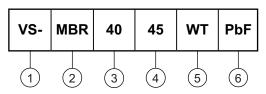
 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ at \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ (1 - D); \ I_R \ at \ V_{R1} = Rated \ V_R \) \\ \end{array}$

VS-MBR4045WTPbF, VS-MBR4045WT-N3

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Schottky MBR series

3 - Current rating (40 = 40 A)

4 - Voltage rating (45 = 45 V)

- Circuit configuration:

Center tap (dual) TO-247

6 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION								
VS-MBR4045WTPbF	25	500	Antistatic plastic tube					
VS-MBR4045WT-N3	25	500	Antistatic plastic tube					

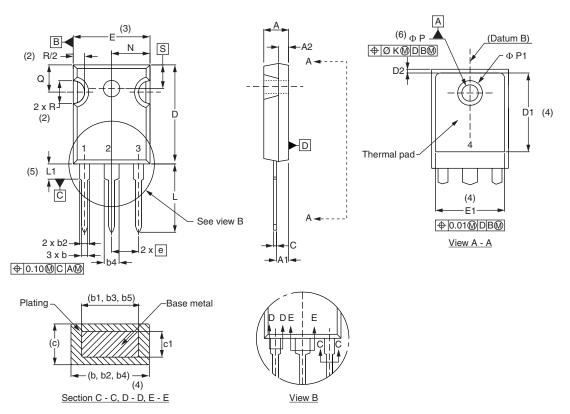
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95223</u>							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					
SPICE model		www.vishay.com/doc?95297					



Vishay Semiconductors

TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS INC		INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	OTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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