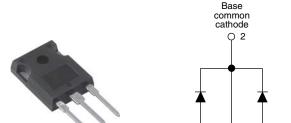


TO-247AC

VS-40L4.CWPbF Series, VS-40L4.CW-N3 Series

Vishay Semiconductors

Schottky Rectifier, 2 x 20 A



Anode

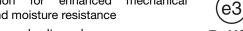
Common cathode

Anode

PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 20 A						
V_{R}	40 V, 45 V						
V _F at I _F	0.49 V						
I _{RM} max.	80 mA at 100 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	20 mJ						

FEATURES

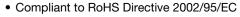
- 150 °C T_J operation
- High purity, high temperature encapsulation for enhanced mechanical strength and moisture resistance





• High frequency operation

· Guard ring for enhanced ruggedness and long term reliability



- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



HALOGEN FREE

DESCRIPTION

The VS-40L...CW... center tap Schottky rectifier has been optimized for very low forward voltage drop with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in parallel switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNITS									
I _{F(AV)}	Rectangular waveform	40	Α						
V_{RRM}		40/45	V						
I _{FSM}	t _p = 5 µs sine	1240	Α						
V _F	20 Apk, T _J = 125 °C (per leg, typical)	0.42	V						
T _J		- 55 to 150	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-40L40CWPbF	VS-40L40CW-N3	VS-40L45CWPbF	VS-40L45CW-N3	UNITS				
Maximum DC reverse voltage	V_R									
Maximum working peak reverse voltage	V _{RWM}	40	40	45	45	V				

ABSOLUTE MAXIMUM RATINGS										
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS					
Maximum average per leg forward current		_	50 % duty cycle at T _C = 122 °C	` rootangular wayafarm	20	А				
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at 1 _C = 122 C	40						
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1240					
		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	350					
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.4 mH		20	mJ				
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		3	Α				



VS-40L4.CWPbF Series, VS-40L4.CW-N3 Series

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS				
Maximum forward voltage drop per leg See fig. 1		20 A	T _{.1} = 25 °C	0.48	0.53	V			
	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.61	0.69				
		20 A	T _{.1} = 125 °C	0.42	0.49				
		40 A	1J=125 C	0.60	0.70				
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	ı	1.5	mA			
See fig. 2	IRM (**)	T _J = 100 °C	VR = nateu VR	20	80	IIIA			
Threshold voltage	V _{F(TO)}	T _{.I} =T _{.I} maximum		0	.27	V			
Forward slope resistance	r _t	ı j = ı j maxımum		8.72		mΩ			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	-	1500	pF				
Maximum voltage rate of change	dV/dt	Rated V _R							

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C				
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4						
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.8	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24					
Approximate weight				6	g				
Approximate weight				0.21	OZ.				
Mounting torque	minimum		New Librianted through	6 (5)	kgf · cm				
Mounting torque	maximum	Non-lubricated threads		12 (10)	(lbf ⋅ in)				
Marking daving			Coop ob to TO 047AC (IEDEC)	40L40CW					
Marking device			Case style TO-247AC (JEDEC)	40L45CW					

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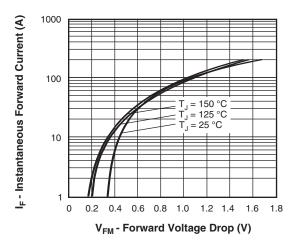


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

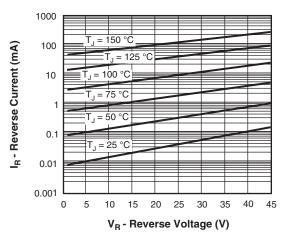


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

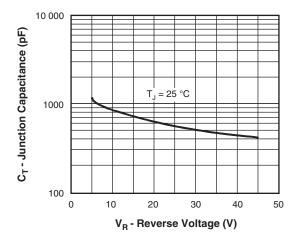


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

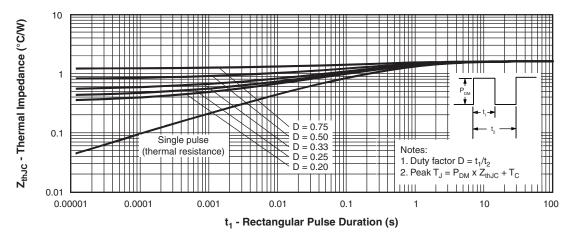


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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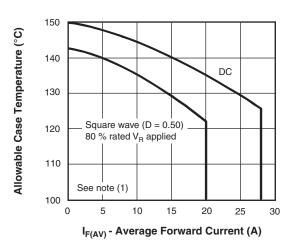


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

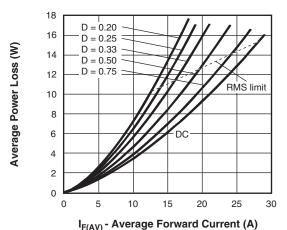


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

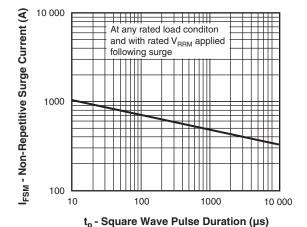


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

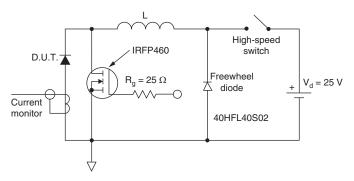


Fig. 8 - Unclamped Inductive Test Circuit

Note

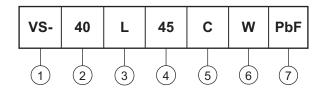
 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at (I_{F(AV)}/D) (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80 \ \%$ rated V_R

VS-40L4.CWPbF Series, VS-40L4.CW-N3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE





Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Schottky "L" series

40 = 40 V 45 = 45 V

Circuit configuration:

C = Common cathode

6 - Package:

W = TO-247

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)										
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPT										
VS-40L40CWPbF	25	500	Antistatic plastic tube							
VS-40L40CW-N3	25	500	Antistatic plastic tube							
VS-40L45CWPbF	25	500	Antistatic plastic tube							
VS-40L45CW-N3	25	500	Antistatic plastic tube							

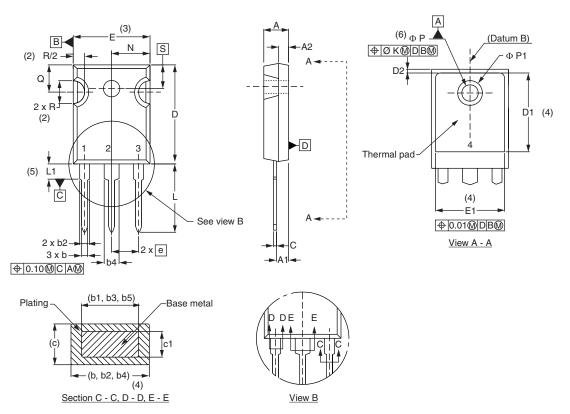
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95223						
Dout moulting information	TO-247AC PbF	www.vishay.com/doc?95226				
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007				



Vishay Semiconductors

TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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