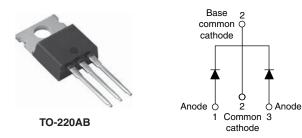


**Vishay Semiconductors** 

## Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY			
Package	TO-220AB		
I <sub>F(AV)</sub>	2 x 20 A		
V <sub>R</sub>	100 V		
V <sub>F</sub> at I <sub>F</sub>	0.67 V		
I <sub>RM</sub> max.	11 mA at 125 °C		
TJ	175 °C		
Diode variation	Common cathode		
E <sub>AS</sub>	7.50 mJ		

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · High frequency operation
- HALOGEN · Guard ring for enhanced ruggedness and long FREE term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	40	A		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A		
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.67	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-43CTQ100PbF	VS-43CTQ100-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	100	100	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	100	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per le		50 % duty cycle at $T_{C}$ = 135 °C, rectangular waveform –		20		
See fig. 5 per devic	e I <sub>F(AV)</sub>			40	A	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse Following any rated load condition and	850			
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	with rated V <sub>RRM</sub> applied	275		
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 60 \text{ mH}$		7.50	mJ	
Repetitive avalanche current per leg I <sub>AR</sub>		Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		0.50	А	



COMPLIANT



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### **ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		20 A	T.I = 25 °C	0.81	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	$-1_{\rm J} = 25^{-1}$	0.98	V
See fig. 1	VFM ("	20 A	T.I = 125 °C	0.67	v
		40 A	$1_{\rm J} = 125$ C	0.81	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated V <sub>B</sub>	1	~^^
See fig. 2	IRM (')	T <sub>J</sub> = 125 °C	$v_{\rm R}$ = Rated $v_{\rm R}$	11	mA
Threshold voltage	V <sub>F(TO)</sub>	T T mavimum		0.71	V
Forward slope resistance	r <sub>t</sub>	ij = ij maximum	$T_J = T_J maximum$		mΩ
Maximum junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1480	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

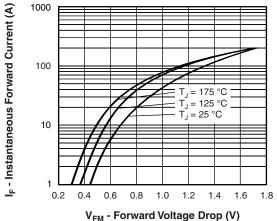
#### Note

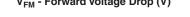
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ige	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistanc junction to case per leg	е,	D		2.0		
Maximum thermal resistanc junction to case per package	,	R <sub>thJC</sub>	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
Mounting torque —	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	43CT	Q100	

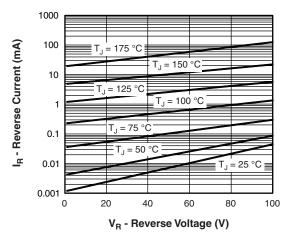


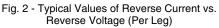
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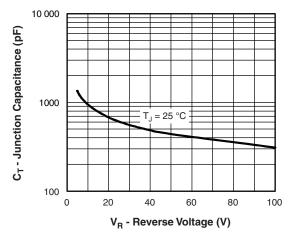
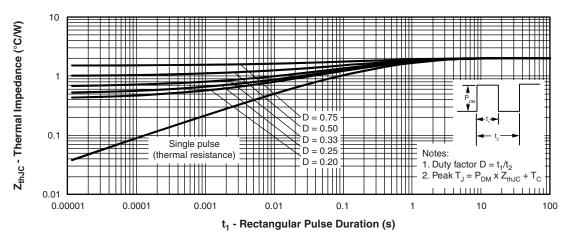


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

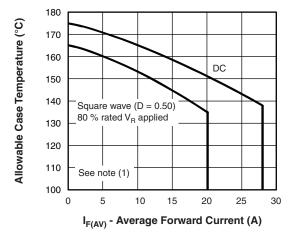


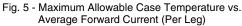


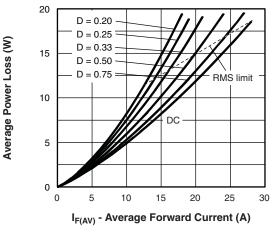
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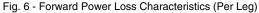


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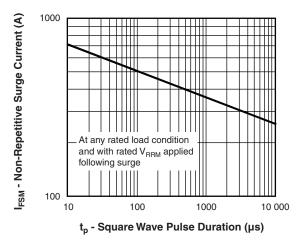


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

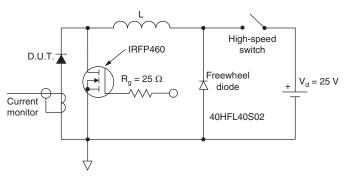


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$ 

Revision: 11-Oct-11

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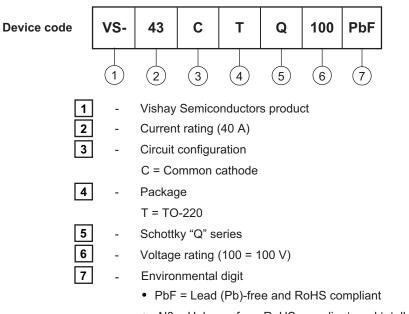
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### **Vishay Semiconductors**

#### **ORDERING INFORMATION TABLE**



-N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-43CTQ100PbF	50	1000	Antistatic plastic tube		
VS-43CTQ100-N3	50	1000	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
	TO-220ABPbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB-N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95065		



**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





.ead	assignments

**Diodes** 

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	LLIMETERS INCHES		NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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