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PRODUCT SUMMARY

Package

 $I_{F(AV)}$

 V_R

V_F at I_F

 I_{RM}

T_J max.

Diode variation

E_{AS}

Cathode

0

SMB

1 A

100 V

0.59 V

1 mA at 125 °C

175 °C

Single die

1.0 mJ

Anode

-0

Schottky Rectifier, 1 A

FEATURES

• Low forward voltage drop



RoHS

COMPLIANT

HALOGEN

FREE

- Guard ring for enhanced ruggedness and long term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | | | | | |
|-----------------------------------|----------------------------------|-------------|-------|--|--|--|--|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | | | | | |
| I _{F(AV)} | Rectangular waveform | 1 | A | | | | | | |
| V _{RRM} | | 100 | V | | | | | | |
| I _{FSM} | t _p = 5 μs sine | 780 | А | | | | | | |
| V _F | 1.0 Apk, T _J = 125 °C | 0.59 | V | | | | | | |
| TJ | Range | - 55 to 175 | ۵°C | | | | | | |

| VOLTAGE RATINGS | | | | | | | |
|--------------------------------------|------------------|---------------|-------|--|--|--|--|
| PARAMETER | SYMBOL | VS-10BQ100-M3 | UNITS | | | | |
| Maximum DC reverse voltage | V _R | 100 | V | | | | |
| Maximum working peak reverse voltage | V _{RWM} | 100 | V | | | | |

| ABSOLUTE MAXIMUM RATINGS | | | | | | |
|--|--------------------|---|--|--------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS | |
| Maximum average forward current | I _{F(AV)} | 50 % duty cycle at T_L = 143 °C | at $T_L = 143 \text{ °C}$, rectangular waveform | | А | |
| Maximum peak one cycle non-repetitive surge current | I _{FSM} | 5 µs sine or 3 µs rect. pulse | Following any rated load condition and with | 780 | A | |
| | | 10 ms sine or 6 ms rect. pulse | rated V_{RRM} applied | 38 | | |
| Non-repetitive avalanche energy | E _{AS} | T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH | | 1.0 | mJ | |
| Repetitive avalanche current | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical | | 0.5 | А | |

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| ELECTRICAL SPECIFICATIONS | | | | | | |
|--|--------------------------------|--|---------------------------------------|-------|------|--|
| PARAMETER | SYMBOL | TEST CO | VALUES | UNITS | | |
| | V _{FM} ⁽¹⁾ | 1 A | T ₁ = 25 °C | 0.75 | V | |
| Maximum forward voltage drop See fig. 1 | | 2 A | - 1j = 25 C | 0.82 | | |
| | | 1 A | T 105 %C | 0.59 | | |
| | | 2 A | T _J = 125 °C | 0.65 | | |
| Maximum reverse leakage current | I _{RM} | T _J = 25 °C | | 0.5 | mA | |
| See fig. 2 | | T _J = 125 °C | V _R = Rated V _R | 1 | | |
| Typical junction capacitance | CT | V_R = 5 V_{DC} , (test signal range 100 kHz to 1 MHz), 25 °C | | 65 | pF | |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body | | 2.0 | nH | |
| Maximum voltage rate of charge | dV/dt | Rated V _R 10 000 | | | V/µs | |

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

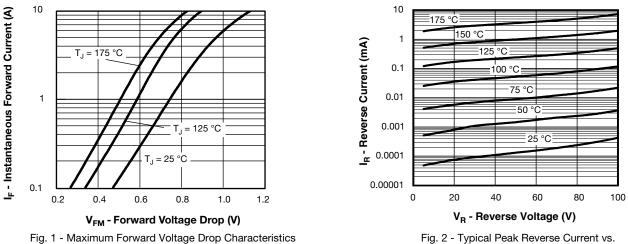
| THERMAL - MECHANICAL SPECIFICATIONS | | | | | |
|---|--|-----------------------------------|-------------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS | |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | - 55 to 175 | °C | |
| Maximum thermal resistance, junction to lead | R _{thJL} ⁽²⁾ | DC operation | 36 | °C/W | |
| Maximum thermal resistance, junction to ambient | R _{thJA} | | 80 | C/W | |
| Approximate weight | | | 0.10 | g | |
| Approximate weight | | | 0.003 | oz. | |
| Marking device | | Case style SMB (similar DO-214AA) | 1 | J | |

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink (1)

(2) Mounted 1" square PCB

2



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g. 2 - Typical Peak Reverse Current v Reverse Voltage

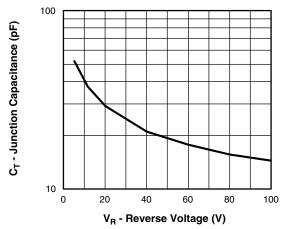
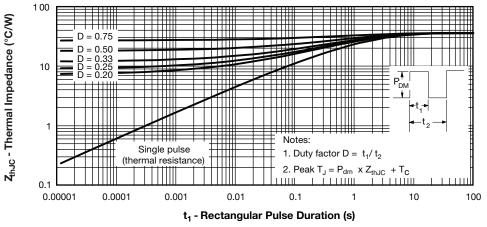
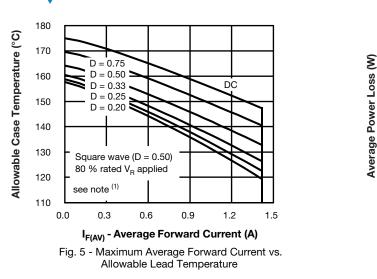


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

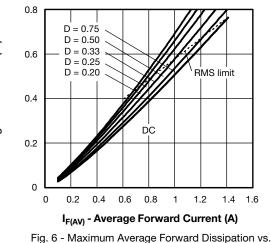








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. 6 - Maximum Average Forward Dissipation vs Average Forward Current

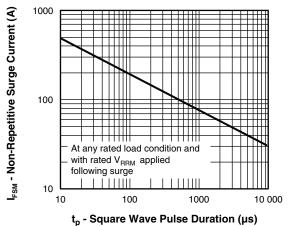


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

⁽¹⁾ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

| Device code | vs- | 1 | 0 | в | Q | 100 | -M3 |
|-------------|-----|---|------|-----------|------------|-----------|---------|
| | | | 2) | 3 | 4 | 5 | 6 |
| | 1 | - | Visł | nay Serr | niconduo | ctors pro | oduct |
| | 2 | - | Cur | rent rati | ng | | |
| | 3 | - | в = | SMB | | | |
| | 4 | - | Q = | Schottk | ty "Q" se | eries | |
| | 5 | - | Volt | age rati | ng (100 | = 100 \ | /) |
| | 6 | - | Env | ironmer | ntal digit | : | |
| | | | -M3 | = Halog | gen-free | , RoHS | complia |

| ORDERING INFORMATION (Example) | | | | | | | | |
|--------------------------------|---|------|------------------------------------|--|--|--|--|--|
| PREFERRED P/N | PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION | | | | | | | |
| VS-10BQ100-M3/5BT | 5BT | 3200 | 13" diameter plastic tape and reel | | | | | |

| LINKS TO RELATED DOCUMENTS | | | | | |
|----------------------------|--------------------------|--|--|--|--|
| Dimensions | www.vishay.com/doc?95401 | | | | |
| Part marking information | www.vishay.com/doc?95403 | | | | |
| Packaging information | www.vishay.com/doc?95404 | | | | |



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