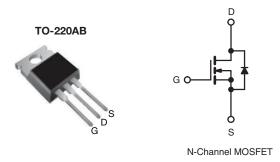


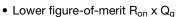
S Series Power MOSFET

PRODUCT SUMMARY					
V _{DS} at T _J max. (V)	650				
R _{DS(on)} max. at 25 °C (Ω)	V _{GS} = 10 V 0.190				
Q _g max. (nC)	98				
Q _{gs} (nC)	17				
Q _{gd} (nC)	25				
Configuration	Single				



FEATURES

- · Generation one
- High E_{AR} capability



- 100 % avalanche tested
- Ultra low R_{on}
- dV/dt ruggedness
- Ultra low gate charge (Qa)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- PFC power supply stages
- · Hard switching topologies
- · Solar inverters
- UPS
- Motor control
- Lighting
- Server telecom

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	SiHP22N60S-E3

PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	600	.,	
Gate-Source Voltage	V _{GS}	± 30	V			
Continues Dunis Comment	V -+ 10 V	T _C = 25 °C		22	А	
Continuous Drain Current	V _{GS} at 10 V	$T_C = 25 ^{\circ}C$ $T_C = 100 ^{\circ}C$	ID	13		
Pulsed Drain Current ^a	I _{DM}	65				
Linear Derating Factor		TO-220AB		2	W/°C	
Single Pulse Avalanche Energy ^b	E _{AS}	690	- m I			
Repetitive Avalanche Energy ^a	E _{AR}	25	– mJ			
Maximum Power Dissipation		TO-220AB	P _D	250	W	
Drain-Source Voltage Slope	T _J = 125 °C		d\//dt	37	1//	
Reverse Diode dV/dt ^d			dV/dt	5.3	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +150	°C	
Soldering Recommendations (Peak Temperature) ^c	for 10 s			300		

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature.
- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 28.2 mH, R_g = 25 Ω , I_{AS} = 7 A.
- c. 1.6 mm from case.
- d. $I_{SD} \le I_D$, $dI/dt = 100 \text{ A/}\mu\text{s}$, starting $T_J = 25 \,^{\circ}\text{C}$.



Vishay Siliconix

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYP.	MAX.	UNIT	
Maximum Junction-to-Ambient	TO-220AB	R _{thJA}	-	62	°C/W	
Maximum Junction-to-Case (Drain)	TO-220AB	R_{thJC}	-	0.5	C/ VV	

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS}	600	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25 °C, I _D = 1 mA		-	0.70	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$		2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	V _{GS} = ± 20 V		-	-	± 100	nA
Gate-Source Leakage		\	$V_{GS} = \pm 30 \text{ V}$	-	-	± 1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =	600 V, V _{GS} = 0 V	-	-	1	
Zero Gate Voltage Drain Gunerit		V _{DS} = 600 V	$V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	-	100	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 11 A	-	0.160	0.190	Ω
Forward Transconductance a	9fs	V _{DS} :	= 50 V, I _D = 13 A	-	9.4	-	S
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0 V,		562	2810	5620	pF
Output Capacitance	C _{oss}		$V_{DS} = 25 \text{ V},$		1480	2960	
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz		6.6	33	66	
Effective Output Capacitance (Time Related)	C _{oss eff.} (TR) ^a	V _{GS} = 0 V	V _{DS} = 0 V to 480 V	-	155	-	
Total Gate Charge	Qg			-	75	110	
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V	$I_D = 22 \text{ A}, V_{DS} = 480 \text{ V}$	-	17	-	nC
Gate-Drain Charge	Q_{gd}			-	25	-	
Turn-On Delay Time	t _{d(on)}		•	-	24	50	
Rise Time	t _r	$V_{DD} = 380 \text{ V}, I_{D} = 22 \text{ A},$ $R_{g} = 9.1 \Omega, V_{GS} = 10 \text{ V}$		-	68	100	ns
Turn-Off Delay Time	t _{d(off)}			-	77	115	
Fall Time	t _f			-	59	90	
Gate Input Resistance	R_g	f = 1 MHz, open drain		0.13	0.65	1.3	Ω
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	22	A
Pulsed Diode Forward Current	I _{SM}			-	-	88	
Diode Forward Voltage	V_{SD}	T _J = 25 °C	C, I _S = 22 A, V _{GS} = 0 V	-	-	1.2	V
Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = I _S , dl/dt = 100 A/μs, V _R = 25 V		-	462	690	ns
Reverse Recovery Charge	Q _{rr}			-	8.3	16	μC
Reverse Recovery Current	I _{RRM}			-	30	60	Α

Note

a. $C_{oss\ eff.}$ (TR) is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DS} .



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

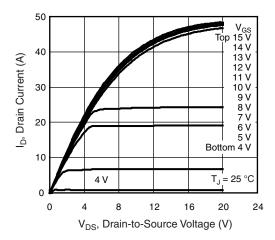


Fig. 1 - Typical Output Characteristics, T_J = 25 °C

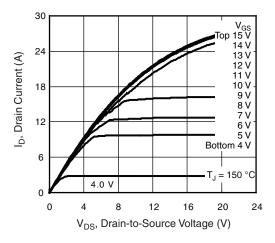


Fig. 2 - Typical Output Characteristics, T_J = 150 °C

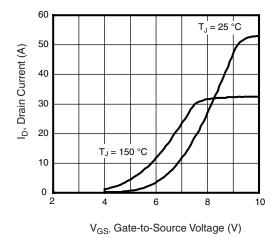


Fig. 3 - Typical Transfer Characteristics

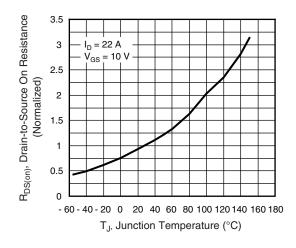


Fig. 4 - Normalized On-Resistance vs. Temperature

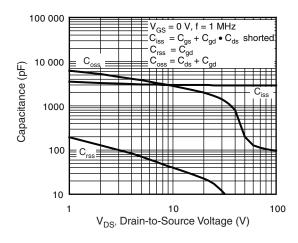


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

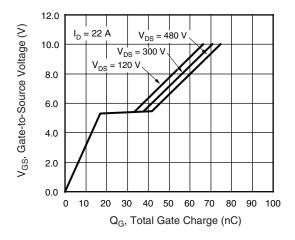


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



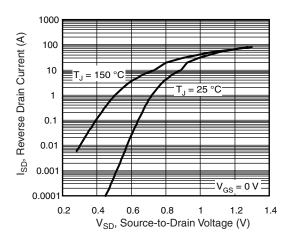


Fig. 7 - Typical Source-Drain Diode Forward Voltage

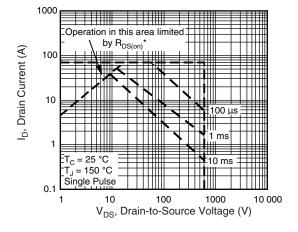


Fig. 8 - Maximum Safe Operating Area

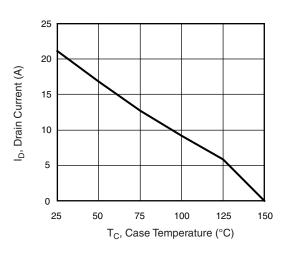


Fig. 9 - Maximum Drain Current vs. Case Temperature

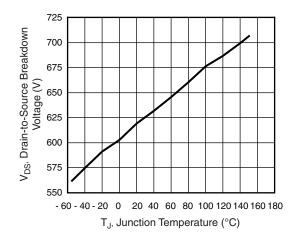


Fig. 10 - Drain-to-Source Breakdown Voltage

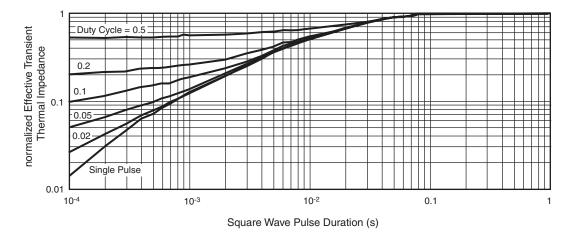


Fig. 11 - Normalized Thermal Transient Impedance, Junction-to-Case



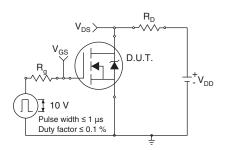


Fig. 12 - Switching Time Test Circuit

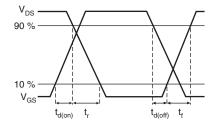


Fig. 13 - Switching Time Waveforms

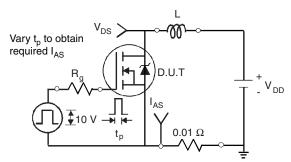


Fig. 14 - Unclamped Inductive Test Circuit

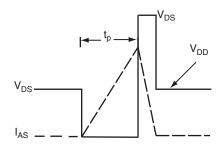


Fig. 15 - Unclamped Inductive Waveforms

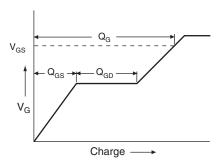


Fig. 16 - Basic Gate Charge Waveform

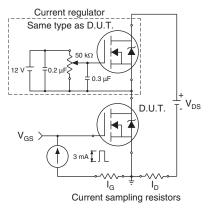
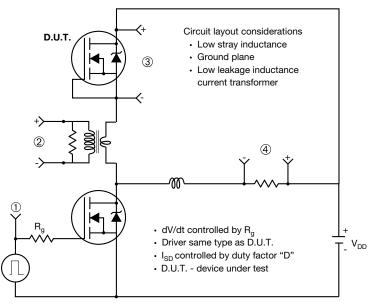


Fig. 17 - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



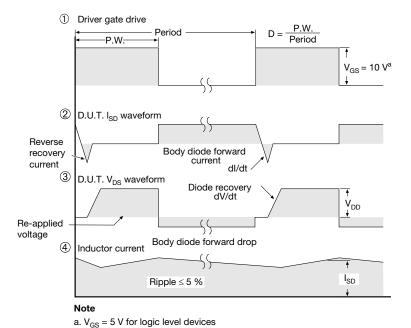


Fig. 18 - For N-Channel

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TO-220-1



DIM	MILLIN	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	4.24	4.65	0.167	0.183		
b	0.69	1.02	0.027	0.040		
b(1)	1.14	1.78	0.045	0.070		
С	0.36	0.61	0.014	0.024		
D	14.33	15.85	0.564	0.624		
E	9.96	10.52	0.392	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.10	6.71	0.240	0.264		
J(1)	2.41	2.92	0.095	0.115		
L	13.36	14.40	0.526	0.567		
L(1)	3.33	4.04	0.131	0.159		
ØР	3.53	3.94	0.139	0.155		
Q	2.54	3.00	0.100	0.118		
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031						

Note

 \bullet $M^{\star}=0.052$ inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



Revison: 14-Dec-15 1 Document Number: 66542



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