COMPLIANT

HALOGEN

FREE



Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier

TMBS® SlimSMATM



Top View

Bottom View

DO-221AC

PRIMARY CHARACTERISTICS			
I _{F(AV)}	5.0 A		
V_{RRM}	50 V		
I _{FSM}	100 A		
V_F at $I_F = 5.0 A$	0.41 V		
T _J max.	150 °C		
Package	DO-221AC (SlimSMA)		
Diode variations	Single die		

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

FEATURES

- Very low profile typical height of 0.95 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

MECHANICAL DATA

Case: DO-221AC (SlimSMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSAF5N50	UNIT	
Device marking code		5N5		
Maximum repetitive peak reverse voltage	V_{RRM}	50	V	
Maximum DC famuard aurent (fig. 1)	I _F ⁽¹⁾	5.0	Α Α	
Maximum DC forward current (fig. 1)	I _F ⁽²⁾	3.0		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	100	А	
Maximum DC reverse voltage	V _{DC}	35	V	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

- (1) Mounted on 10 mm x 10 mm pad areas, 2 oz. FR4 PCB
- (2) Free air, mounted on recommended copper pad area



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.5 A	T _A = 25 °C	V _F (1)	0.41	-	V
	$I_F = 5.0 A$			0.48	0.56	
	$I_F = 2.5 A$	T _A = 125 °C		0.31	=	
	$I_F = 5.0 A$			0.41	0.50	
Reverse current	V _R = 35 V	T _A = 25 °C	I _R (2)	0.02	=	mA
	v _R = 35 v	T _A = 125 °C		12	=	
	V - 50 V	T _A = 25 °C		-	1.4	
	$V_R = 50 \text{ V}$	T _A = 125 °C		19	50	
Typical junction capacitance	4.0 V, 1 MF	4.0 V, 1 MHz		850	=	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)				
PARAMETER	SYMBOL	VSSAF5N50	UNIT	
Typical thormal registeres	R _{0JA} (1)	115	°C/W	
Typical thermal resistance	R _{0JM} (1)	12		

Note

 $^{(1)}$ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSAF5N50-M3/6A	0.032	6A	3500	7" diameter plastic tape and reel	
VSSAF5N50-M3/6B	0.032	6B	14 000	13" diameter plastic tape and reel	

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

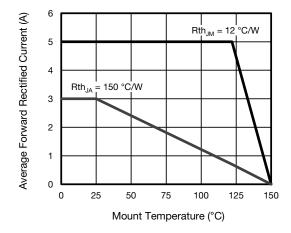


Fig. 1 - Maximum Forward Current Derating Curve

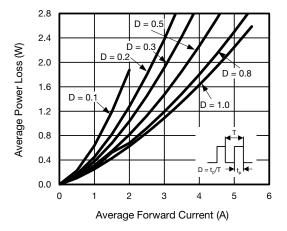


Fig. 2 - Average Power Loss Characteristics



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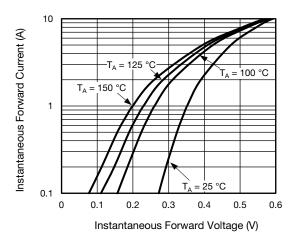


Fig. 3 - Typical Instantaneous Forward Characteristics

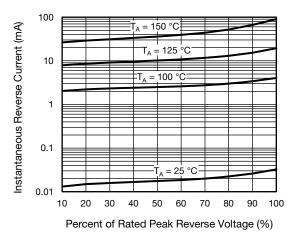


Fig. 4 - Typical Reverse Leakage Characteristics

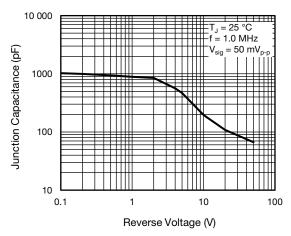


Fig. 5 - Typical Junction Capacitance

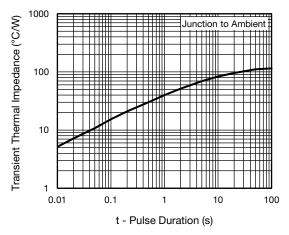


Fig. 6 - Typical Transient Thermal Impedance

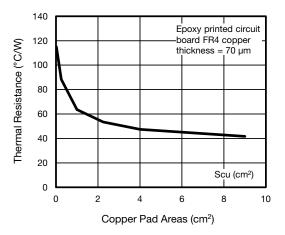


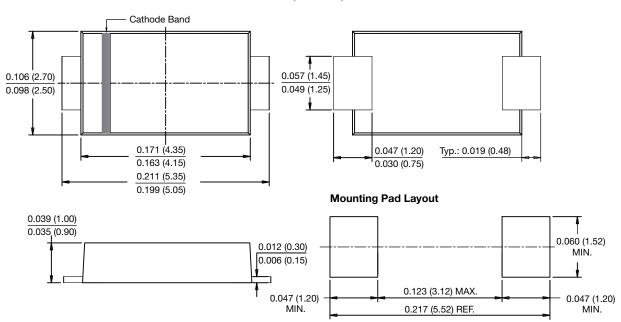
Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-221AC (SlimSMA)





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