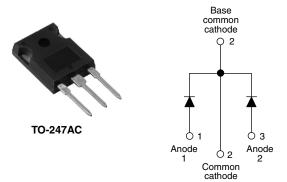


## VS-STPS40L45CWPbF, VS-STPS40L45CW-N3

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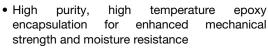
## Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY							
Package	TO-247AC						
I <sub>F(AV)</sub>	2 x 20 A						
V <sub>R</sub>	45 V						
V <sub>F</sub> at I <sub>F</sub>	0.49 V						
I <sub>RM</sub> max.	80 mA at 100 °C						
T <sub>J</sub> max.	150 °C						
Diode variation	Common cathode						
E <sub>AS</sub>	20 mJ						

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### **DESCRIPTION**

The VS-STPS40L45CW... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	40	Α						
$V_{RRM}$		45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1240	Α						
V <sub>F</sub>	20 Apk, T <sub>J</sub> = 125 °C (per leg, typical)	0.42	V						
T <sub>J</sub>		- 55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-STPS40L45CWPbF	VS-STPS40L45CW-N3	UNITS				
Maximum DC reverse voltage	$V_R$	45	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average per device forward current		50 % duty cycle at T <sub>C</sub> = 122 °C, rectangular waveform		40				
See fig. 5 per leg	I <sub>F(AV)</sub>	00 % daty cycle at 10 = 122 0	20	Α				
Maximum peak one cycle non-repetitive surge current per leg	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1240				
See fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	350				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 4.4 mH		20	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \text{ x } V_R$ typical		3	А			



# VS-STPS40L45CWPbF, VS-STPS40L45CW-N3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDIT	TYP.	MAX.	UNITS			
		20 A	T. <sub>.</sub> = 25 °C	0.48	0.53	V		
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	11 = 25 0	0.61	0.69			
See fig. 1	VFM ('')	20 A	T. <sub>1</sub> = 125 °C	0.42	0.49			
		40 A	1J = 125 C	0.60	0.70			
Reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	-	1.5	mA		
See fig. 2		T <sub>J</sub> = 100 °C	v <sub>R</sub> = nated v <sub>R</sub>	20	80	IIIA		
Threshold voltage	V <sub>F(TO)</sub>	0.27		27	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		8.	72	mΩ		
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 10	=	1500	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm fr	7.5	-	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000			000	V/µs		

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C				
Maximum thermal resistance, junction to case per leg	В	DC operation See fig. 4	1.6					
Maximum thermal resistance, junction to case per package	- R <sub>thJC</sub>	DC operation	0.8	°C/W				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24					
Approximate weight			6	g				
Approximate weight			0.21	OZ.				
Mounting torque minimum		Non-lubricated threads	6 (5)	kgf · cm				
Mounting torque maximum		inon-iubricateu trireaus	12 (10)	(lbf $\cdot$ in)				
Marking device		Case style TO-247AC (JEDEC)	STPS40	L45CW				

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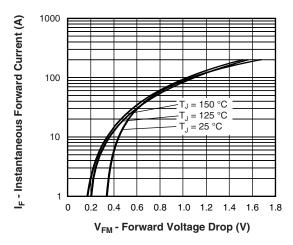


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

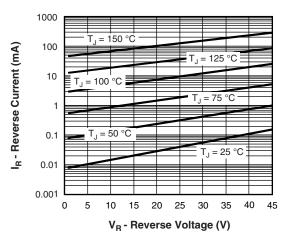


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

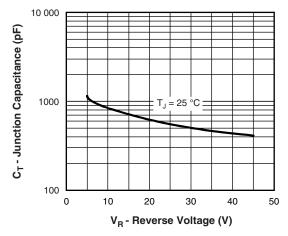


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

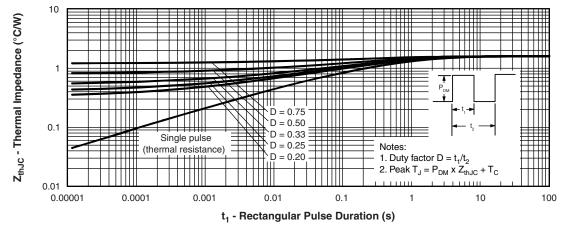


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



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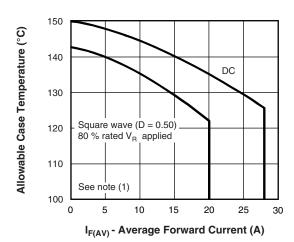


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

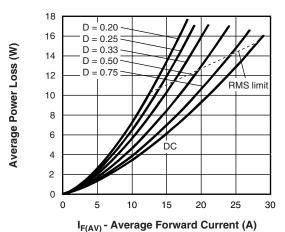


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

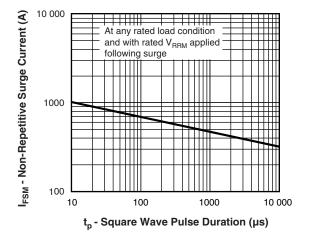


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

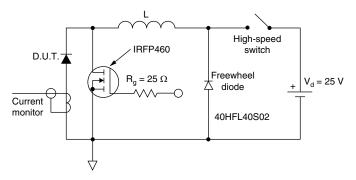


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

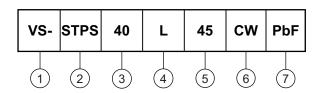
 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

## VS-STPS40L45CWPbF, VS-STPS40L45CW-N3

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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Schottky STPS series

3 - Current ratings (40 = 40 A)

L = Low forward voltage

5 - Voltage code (45 = 45 V)

6 - Package:

CW = TO-247

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION									
VS-STPS40L45CWPbF	25	500	Antistatic plastic tube						
VS-STPS40L45CW-N3	25	500	Antistatic plastic tube						

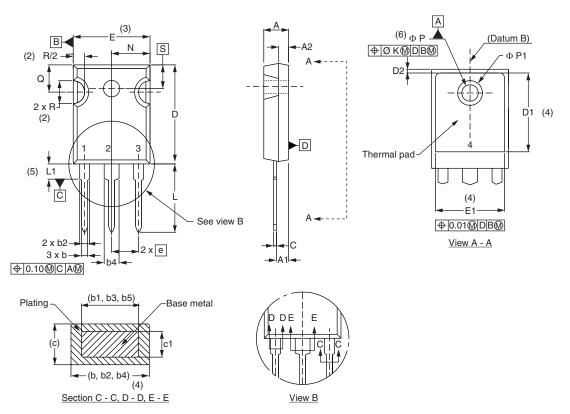
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95223</u>							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					



## Vishay Semiconductors

### **TO-247**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS INCHES		INCHES		NOTES S	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	E3	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$  Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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