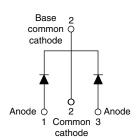


## VS-40CTQ045PbF, VS-40CTQ045-N3

Vishay Semiconductors

# Schottky Rectifier, 2 x 20 A

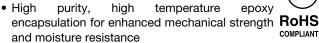




PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 20 A				
$V_{R}$	45 V				
V <sub>F</sub> at I <sub>F</sub>	0.48 V				
I <sub>RM</sub> max.	115 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	20 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Very low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long FREE term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

## **DESCRIPTION**

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	Α			
V <sub>RRM</sub>		45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	1240	Α			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.48	V			
T <sub>J</sub>	Range	- 55 to 150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-40CTQ045PbF	VS-40CTQ045-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	45	45	V		
Maximum working peak reverse voltage	$V_{RWM}$	45	43	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current per leg		50 % duty cycle at T <sub>C</sub> = 116 °C, rectangular waveform		20		
See fig. 5 per device	I <sub>F(AV)</sub>			40		
Maximum peak one cycle non-repetitive surge current per leg	1	5 μs sine or 3 μs rect. pulse	Ilse Following any rated load condition and with rated		Α	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	350		
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_{J} = 25  ^{\circ}\text{C},  I_{AS} = 3  \text{A},  L = 4.4  \text{mH}$		20	mJ	
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	Α	



# **VS-40CTQ045PbF, VS-40CTQ045-N3**

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
		20 A	T <sub>.1</sub> = 25 °C	0.53	V	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 C	0.68		
See fig. 1	V <sub>FM</sub> (1)	20 A	T 105 °C	0.48		
		40 A	T <sub>J</sub> = 125 °C	0.67		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V Detectiv	3	A	
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	115	mA	
Threshold voltage	V <sub>F(TO)</sub>	T T mayimum		0.27	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		8.72	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2800	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range	•	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation	2.0		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Manuskanakanan	minimum			6 (5)	kgf · cm	
Mounting torque –	maximum			12 (10)	(lbf $\cdot$ in)	
Marking device			Case style TO-220AB	40CT	Q045	

## Vishay Semiconductors

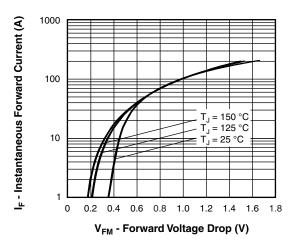


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

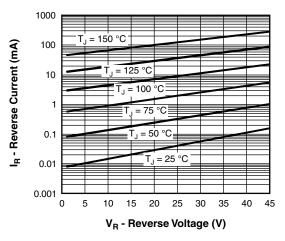


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

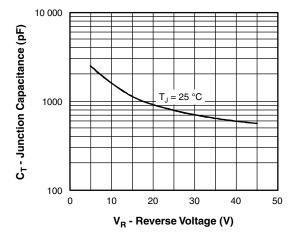


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

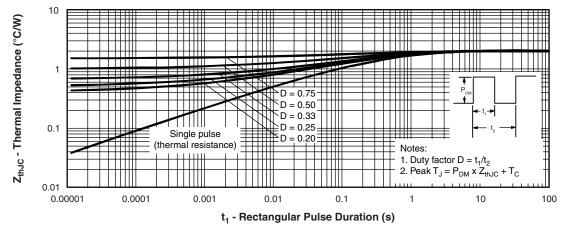


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

## www.vishay.com

## Vishay Semiconductors

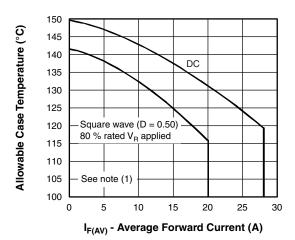


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

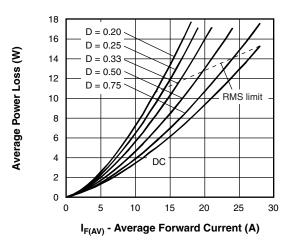


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

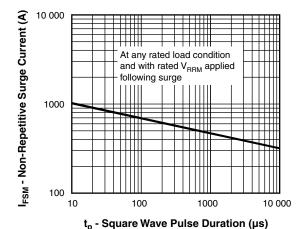


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

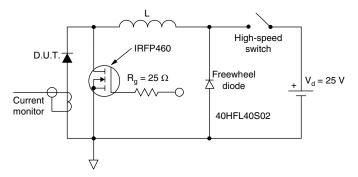


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

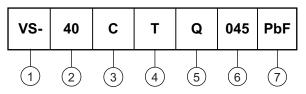
 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at (I\_{F(AV)}/D) (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 10 \ V$ 

# **VS-40CTQ045PbF, VS-40CTQ045-N3**

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (045 = 45 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-40CTQ045PbF	50	1000	Antistatic plastic tube				
VS-40CTQ045-N3	50	1000	Antistatic plastic tube				

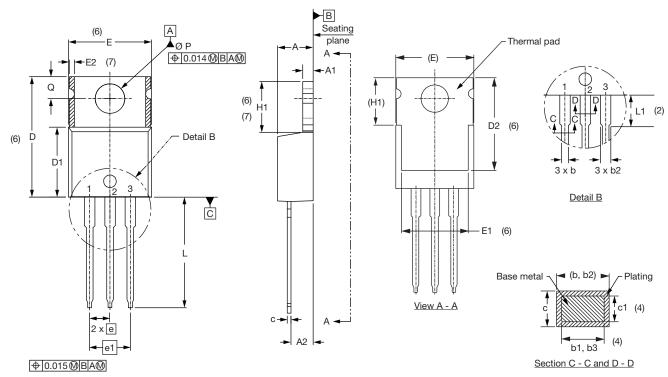
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95222</u>					
Dort marking information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



## Vishay Semiconductors

## **TO-220AB**

#### **DIMENSIONS** in millimeters and inches



## Lead assignments

### **Diodes**

- 1. Anode/open
- 2. Cathode
- 3. Anode

#### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	MILLIMETERS		HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIM	IETERS	RS INCHES		NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
		•	•	•	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



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