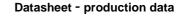


T16T

Snubberless™, logic level and standard 16 A Triacs



Description

Available in through-hole, the T16T series of Triacs can be used as on/off or phase angle control function in general purpose ac switching where high commutation capability is required.

This series can be designed-in in many value sensitive appliances thanks to the parameters guidance provided in the following pages.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

Table 1. Device summary

Order code	Symbol	Value
T1610T-6I	l _{GT} 3Q logic level	10 mA
T1620T-6I T1635T-6I	l _{GT} 3Q Snubberless	20 / 35 mA

A2 Go A1 A1 TO-220AB insulated (T16xxT-6l)

Features

- Medium current Triac
- High static and dynamic commutation
- Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- 600 V V_{RM}
- UL certified (ref. file E81734)

Applications

- Value sensitive application
- General purpose ac line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection



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This is information on a product in full production.

1 Characteristics

Table	2. Absolute maximum ratings (limiting values; $T_j = 2$	25 °C, unless othe	erwise speci	ified)

Symbol	Parameter	Value	Unit			
I _{T(RMS)}	On-state rms current (full sine wave)		T _c = 86 °C	16	А	
I	Non repetitive surge peak on-state current (full	F = 50 Hz	t _p = 20 ms	120	А	
I _{TSM}	cycle, T _j initial = 25 °C)	F = 60 Hz	t _p = 16.7 ms	126	A	
l²t	l^2 t Value for fusing $t_p = 10 \text{ ms}$				A²s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 60 \text{ Hz}$ T $t_r \le 100 \text{ ns}$		T _j = 125 °C	50	A/µs	
V _{DSM} / V _{RSM}	Non repetitive surge peak off-state $t_p = 10 \text{ ms}$ $T_j = 25 \text{ °C}$		T _j = 25 °C	V _{DRM} /V _{RRM} + 100	V	
I _{GM}	Peak gate current $t_p = 20 \ \mu s$ $T_j = 125 \ ^{\circ}C$		4	А		
P _{G(AV)}	Average gate power dissipation	1	W			
T _{stg}	Storage junction temperature range	- 40 to + 150	°C			
Тj	Operating junction temperature range			- 40 to + 125	°C	



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Symbol	Test conditions	Quadrant	Quedrant		Unit			
Symbol	Test conditions Quadrant			T1610T	T1620T	T1635T	Unit	
I _{GT} ⁽¹⁾	$V_{\rm D} = 12 \text{V} \text{R}_{\rm I} = 30 \text{W}$	1 - 11 - 111	MAX.	10	20	35	mA	
'GT ` ′	$v_{\rm D} = 12 v R_{\rm L} = 30 v v$	IV					ШA	
V _{GT}	$V_D = V_{DRM}, R_L = 3.3 \text{ kW},$ $T_j = 25 \text{ °C}$	ALL	MAX.		1.3		V	
V _{GD}	$V_{D} = V_{DRM}, R_{L} = 3.3 \text{ kW},$ $T_{j} = 125 \text{ °C}$	ALL	MIN.	0.2		V		
I _H ⁽²⁾	I _T = 500 mA	I	MAX.	12	25	40	mA	
	I _G = 1.2 I _{GT}	1 - 111	MAX.	20	35	50	mA	
١ _L		IV						
		II		30	40	80		
dV/dt ⁽²⁾	V _D = 67% V _{DRM,} gate open	T _j = 125 °C	MIN.	100	1000	2000)//ue	
uv/ut V	$v_{\rm D} = 07.\% v_{\rm DRM}$, gate open	$T_j = 150 \ ^{\circ}C^{(3)}$	IVIIIN.	20	500	1000	V/µs	
	(dV/dt)c = 0.1 V/µs			8				
	(dV/dt)c = 10 V/µs	T _j = 125 °C		4				
(di/dt)c ⁽²⁾	Without snubber		MIN.		6	16	A/mo	
	(dV/dt)c = 0.1 V/µs		IVIIIN.	3			A/ms	
	(dV/dt)c = 10 V/µs	$T_j = 150 \ ^{\circ}C^{(3)}$		1				
	Without snubber				3	12		

Table 3. Electrical characteristics (T_i = 25 °C, unless otherwise specified)

1. minimum I_{GT} is guaranted at 5% of I_{GT} max.

2. for both polarities of A2 referenced to A1.

3. derating information for excess temperature above T_i max.

Table 4. Static characteristics

Symbol	Tes	Value	Unit		
V _T ⁽¹⁾	I _{TM} = 22.6 A, t _p = 380 μs	T _j = 25 °C	MAX.	1.55	V
V _{TO} ⁽¹⁾	Threshold voltage	T _j = 125 °C	MAX.	0.85	V
R _D ⁽¹⁾	Dynamic resistance	T _j = 125 °C	MAX.	30	mΩ
	$V_{DRM} = V_{RRM}$	T _j = 25 °C	MAX.	5	μA
I _{DRM}		T _j = 125 °C		1	
I _{RRM}	$V_{D} = 0.9 \times V_{DRM}$	$T_j = 150 \ ^{\circ}C^{(2)}$	TYP.	1.9	mA

1. for both polarities of A2 referenced to A1.

2. derating information for excess temperature above T_j max.



Symbol	Parameter Value Uni						
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W				
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W				

Table 5 Thermal resistance

Figure 1. Maximum power dissipation versus rms on-state current (full cycle)

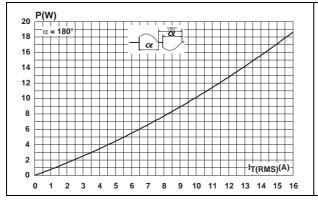


Figure 3. On-state rms current versus ambient temperature

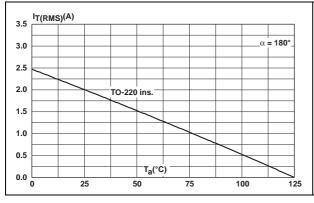
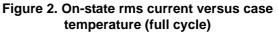


Figure 5. On state characteristics (maximum values)



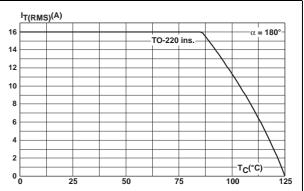


Figure 4. Relative variation of thermal impedance versus pulse duration

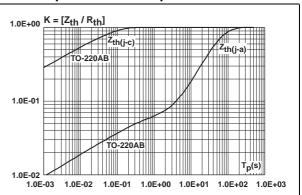


Figure 6. Surge peak on state current versus number of cycles

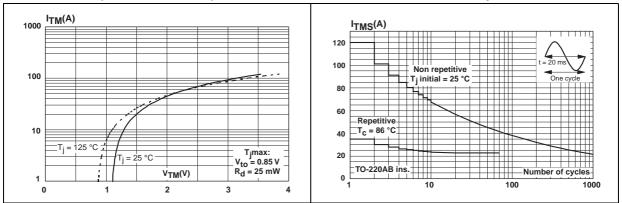




Figure 7. Non repetitive surge peak on state current for a sinusoidal

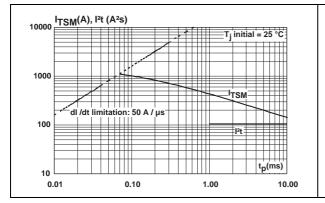
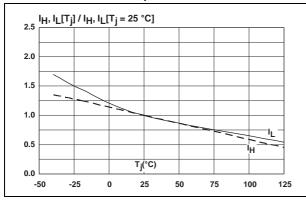
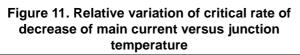
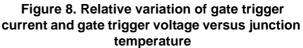


Figure 9. Relative variation of holding current and latching current versus junction temperature







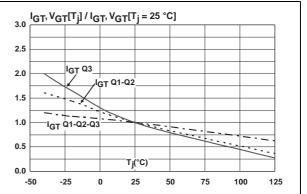


Figure 10. Relative variation of critical rate of decrease of main current versus junction temperature

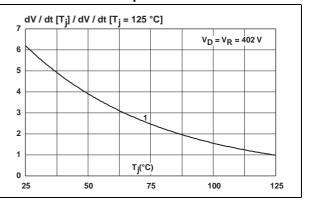
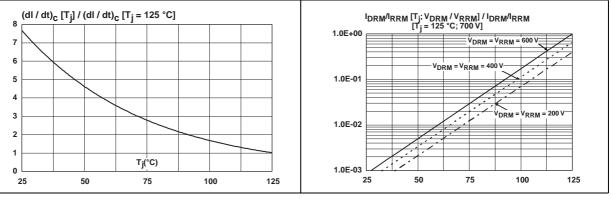


Figure 12. Leakage current versus junction temperature for different values of blocking voltage (typical values)





2 Ordering information scheme

Figure 13. Ordering information scheme

	T I	16 	10 	T	-	6 	ļ
TRIAC							
Current							
16 = 16 A							
Sensitivity							
10 = 10 mA							
20 = 20 mA							
35 = 35 mA							
Application specific							
Voltage							
6 = 600 V							
Package							
I = TO-220AB-Ins.							



3 Package mechanical data

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

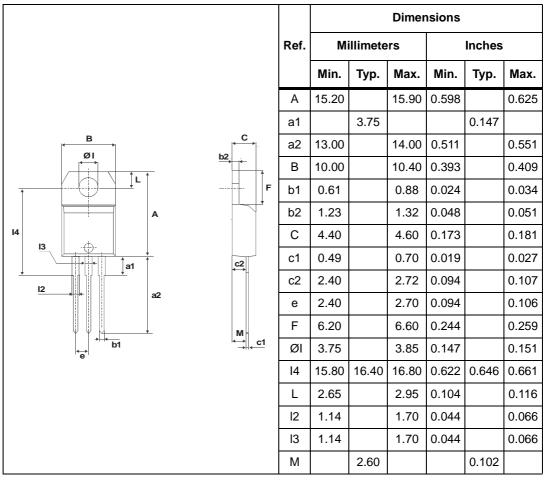


Table 6. TO-220AB insulated dimensions



4 Ordering information

······································						
Order code	Marking	Package We		Base qty	Delivery mode	
T1610T-6I	T1610T-6I					
T1620T-6I	T1620T-6I	TO-220AB ins.	2.3 g	50	Tube	
T1635T-6I	T1635T-6I					

Table 7. Ordering information

5 Revision history

Date	Revision	Changes	
03-Dec-2009	1	Initial release.	
18-Jan-2010	2	Updated pag.1.	
19-Jun-2014	3	Updated features in cover page.	

Table 8. Document revision history



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