

Trench gate field-stop IGBT, HB series 650 V, 40 A high speed

Datasheet - production data

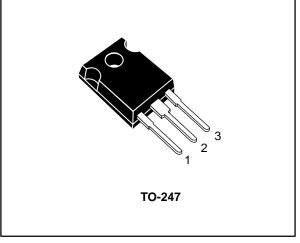
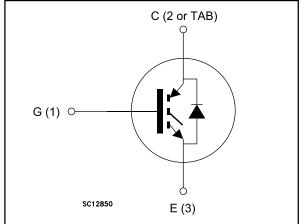


Figure 1: Internal schematic diagram



Features

- Maximum junction temperature: T_J = 175 °C
- High speed switching series
- Minimized tail current
- Low saturation voltage: $V_{CE(sat)} = 1.6 V (typ.)$ @ I_C = 40 A
- Tight parameter distribution
- Safe paralleling
- Low thermal resistance
- Very fast soft recovery antiparallel diode

Applications

- Photovoltaic inverters
- High frequency converters

Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the new HB series of IGBTs, which represents an optimum compromise between conduction and switching loss to maximize the efficiency of any frequency converter. Furthermore, the slightly positive $V_{CE(sat)}$ temperature coefficient and very tight parameter distribution result in safer paralleling operation.

Table 1: Device summary

Order code	Marking	Package	Packing
STGW40H65DFB	GW40H65DFB	TO-247	Tube

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This is information on a product in full production.

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1 Electrical ratings

 Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
VCES	Collector-emitter voltage (V _{GE} = 0)	650	V
la la	Continuous collector current at T _C = 25 °C	80	А
lc	Continuous collector current at T _c = 100 °C	40	A
ICP ⁽¹⁾	Pulsed collector current	160	А
V_{GE}	Gate-emitter voltage	±20	V
1_	Continuous forward current at $T_C = 25 \ ^{\circ}C$	80	А
IF	Continuous forward current at T _C = 100 °C	40	A
I _{FP} ⁽¹⁾	Pulsed forward current	160	А
Ртот	Total dissipation at $T_C = 25 \ ^{\circ}C$	283	W
Tstg	Storage temperature range	- 55 to 150	°C
TJ	Operating junction temperature range	- 55 to 175	C

Notes:

 $^{(1)}\mbox{Pulse}$ width limited by maximum junction temperature.

Table 3: Thermal data

Symbol	Parameter	Value	Unit
RthJC	Thermal resistance junction-case IGBT	0.53	
RthJC	Thermal resistance junction-case diode	1.14	°C/W
RthJA	Thermal resistance junction-ambient	50	



 $T_C = 25$ °C unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage	$V_{GE} = 0 V$, $I_C = 2 mA$	650			V
		V_{GE} = 15 V, I_{C} = 40 A		1.6	2	
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 40 A, T _J = 125 °C		1.7		V
		V_{GE} = 15 V, I _C = 40 A, T _J = 175 °C		1.8		
		I _F = 40 A		1.7	2.45	
VF	Forward on-voltage	$I_F = 40 \text{ A}, T_J = 125 ^\circ\text{C}$		1.4		V
		I _F = 40 A, T _J = 175 °C		1.3		
$V_{\text{GE(th)}}$	Gate threshold voltage	$V_{CE} = V_{GE}, I_C = 1 \text{ mA}$	5	6	7	V
I _{CES}	Collector cut-off current	$V_{GE} = 0 V, V_{CE} = 650 V$			25	μA
I _{GES}	Gate-emitter leakage current	$V_{CE} = 0 \text{ V}, \text{ V}_{GE} = \pm 20 \text{ V}$			±250	nA

Table 4: Static characteristics

Table 5: Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Cies	Input capacitance		-	5412	-	
Coes	Output capacitance	Vce= 25 V, f = 1 MHz, Vge = 0 V	-	198	-	pF
Cres	Reverse transfer capacitance		-	107	-	
Qg	Total gate charge		-	210	I	
Q _{ge}	Gate-emitter charge	$V_{CC} = 520 \text{ V}, \text{ Ic} = 40 \text{ A}, \text{ V}_{GE} = 15 \text{ V}$ (see Figure 29: " Gate charge test	-	39	-	nC
Q _{gc}	Gate-collector charge	circuit")	-	82	-	

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time			40	-	
tr	Current rise time			13	-	ns
(di/dt) _{on}	Turn-on current slope	$V_{CE} = 400 \text{ V}, \text{ Ic} = 40 \text{ A}, \text{ V}_{GE} = 15 \text{ V},$ $R_G = 5 \Omega$ (see Figure 28: "Test circuit for inductive load switching")		2413	-	A/µs
t _{d(off)}	Turn-off-delay time			142	-	
t _f	Current fall time			27	-	ns



Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
E _{on} ⁽¹⁾	Turn-on switching energy			498	-	
E _{off} ⁽²⁾	Turn-off switching energy			363	-	μJ
E _{ts}	Total switching energy			861	-	
t _{d(on)}	Turn-on delay time			38	-	ns
tr	Current rise time			14	-	115
(di/dt) _{on}	Turn-on current slope			2186	-	A/µs
$t_{d(off)}$	Turn-off-delay time	V _{CE} = 400 V, I _C = 40 A, V _{GE} = 15 V,		141	-	ns
t _f	Current fall time	$R_G = 5 \Omega$, $T_J = 175 °C$ (see Figure 28: " Test circuit for inductive load switching")		61	-	115
Eon ⁽¹⁾	Turn-on switching energy			1417	-	
E _{off} ⁽²⁾	Turn-off switching energy			764	-	μJ
Ets	Total switching energy			2181	-	

Notes:

 $\ensuremath{^{(1)}}\xspace$ Including the reverse recovery of the diode.

⁽²⁾Including the tail of the collector current.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
trr	Reverse recovery time		-	62	-	ns
Qrr	Reverse recovery charge		-	99	-	nC
Irrm	Reverse recovery current	$I_F = 40 \text{ A}, V_R = 400 \text{ V}, V_{GE} = 15 \text{ V},$ di/dt = 100 A/ μ s (see <i>Figure 28: "Test</i> <i>circuit for inductive load switching"</i>)	-	3.3	-	А
dl _{rr} /dt	Peak rate of fall of reverse recovery current during t _b		-	187	-	A/µs
Err	Reverse recovery energy		-	68	-	μJ
trr	Reverse recovery time	I _F = 40 A, V _R = 400 V, V _{GE} = 15 V, T _J = 175 °C, di/dt = 100 A/μs	-	310	-	ns

Table 7: Diode switching characteristics (inductive load)



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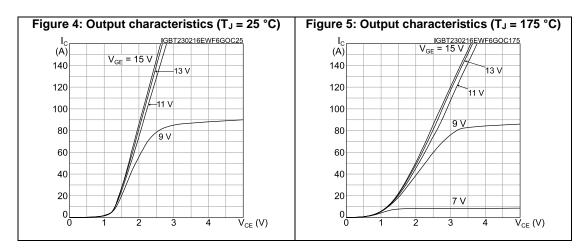
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	Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	Qrr	Reverse recovery charge	(see Figure 28: " Test circuit for inductive load switching")	-	1550	-	nC
	Irrm	Reverse recovery current		-	10	-	А
	dl _{rr} /dt	Peak rate of fall of reverse recovery current during t _b		-	70	-	A/µs
	Err	Reverse recovery energy		-	674	-	μJ

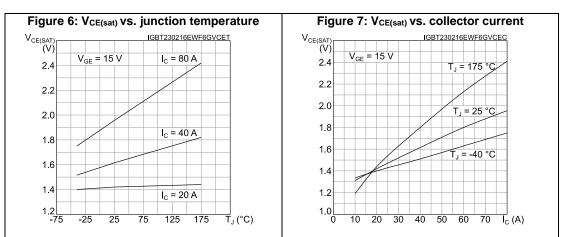


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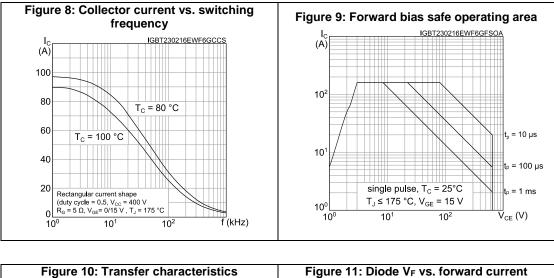
Electrical characteristics (curves) Figure 2: Power dissipation vs. case Figure 3: Collector current vs. case temperature temperature IGBT230216EWF6GPDT V_{GE} = 15 V, T_J ≤ 175 °C IGBT230216EWF6GCCT V_{GE} = 15 V, T_J ≤ 175 °C P_{TOT} (W) I_С (А) 250 80 200 60 150 40 100 20 50 ٥L oL 50 75 100 125 150 50 75 100 125 150 T_c (°C) 25 T_c (°C) 25

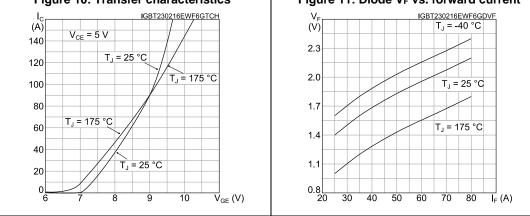


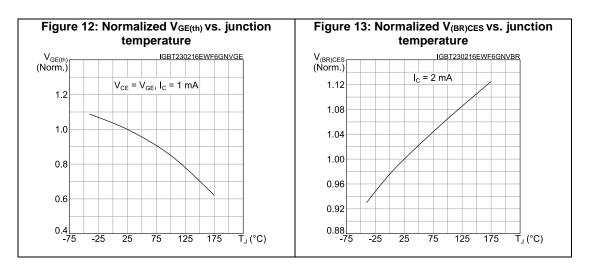


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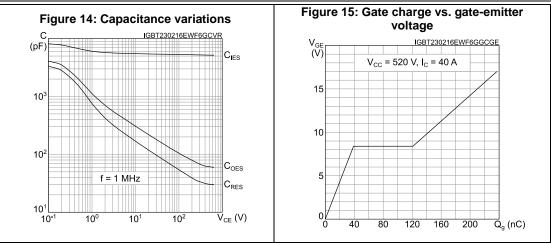


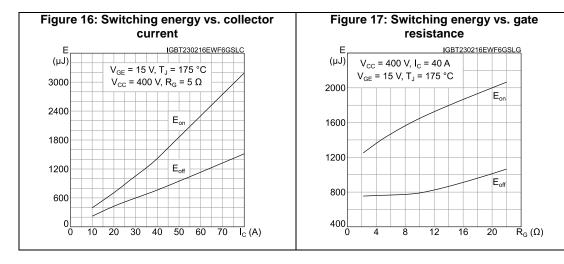


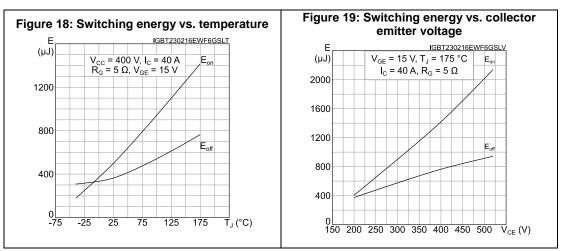


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Electrical characteristics

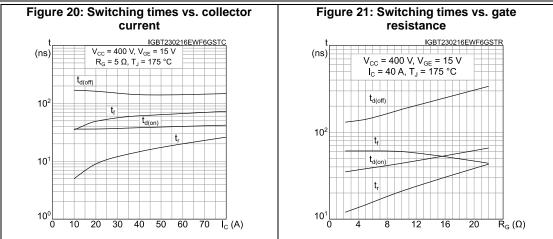


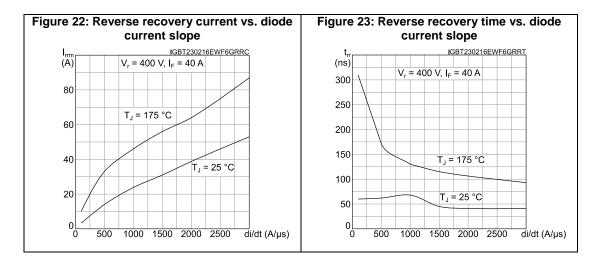


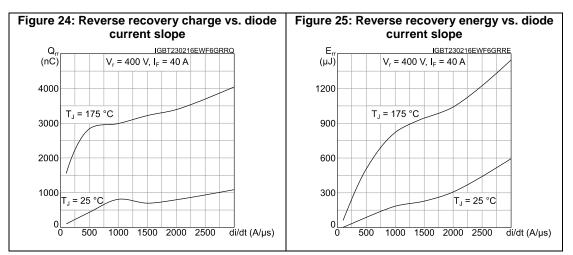


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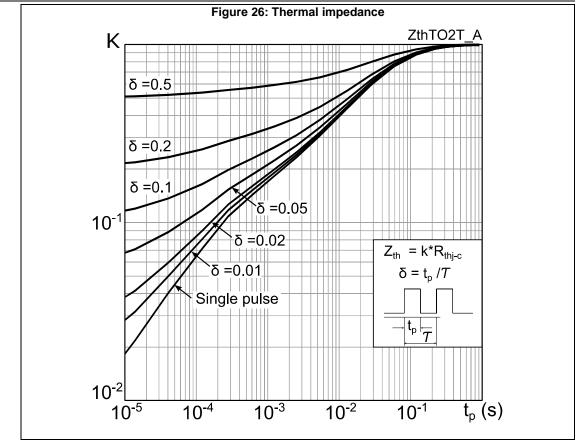






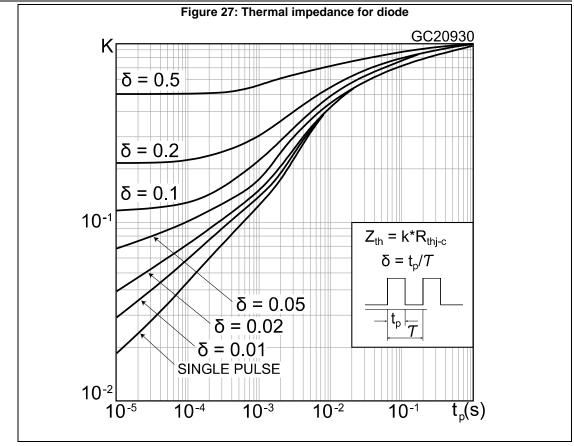
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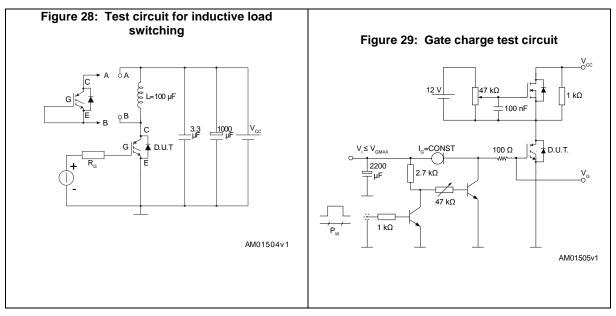


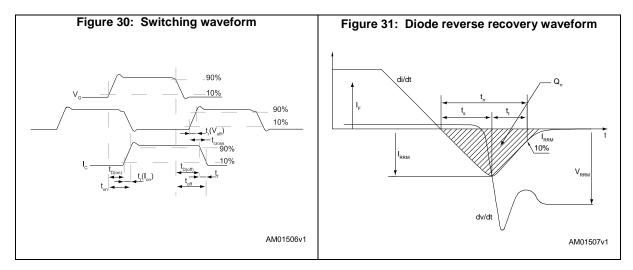
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3 Test circuits



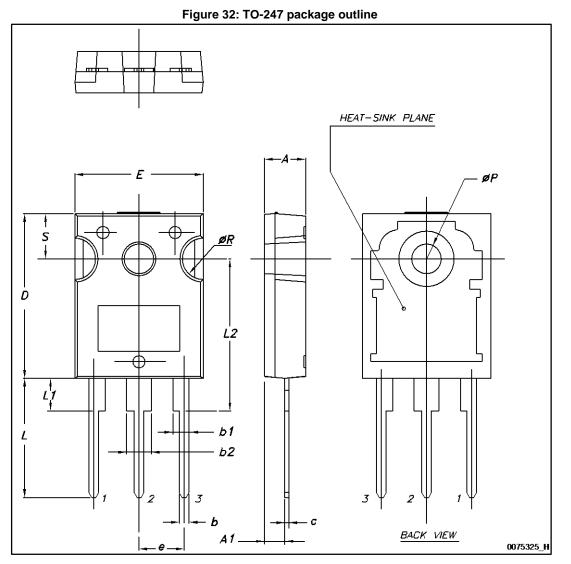




4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

4.1 TO-247 package information





Package information

Table 8: TO-247 package mechanical data						
Dim		mm				
Dim.	Min.	Тур.	Max.			
A	4.85		5.15			
A1	2.20		2.60			
b	1.0		1.40			
b1	2.0		2.40			
b2	3.0		3.40			
С	0.40		0.80			
D	19.85		20.15			
E	15.45		15.75			
е	5.30	5.45	5.60			
L	14.20		14.80			
L1	3.70		4.30			
L2		18.50				
ØP	3.55		3.65			
ØR	4.50		5.50			
S	5.30	5.50	5.70			



5 Revision history

Table 9: Document revision history

Date	Revision	Changes
12-Mar-2013	1	Initial release.
09-Sep-2013	2	 Modified: VCE(sat) values in cover page Modified: VCE(sat), VF and VGE(th) typical and max values in Table Modified: entire typical values in Table 5, 6 and 7 Minor text changes Added: Section 2.1: Electrical characteristics (curves)
11-Sep-2013	3	- Updated TSTG value in Table 2: Absolute maximum ratings.
23-Sep-2013	4	 Updated units in Table 6: IGBT switching characteristics (inductive load).
31-Oct-2013	5	Updated VCE(sat) in Table 4: Static characteristics.
24-Feb-2014	6	Updated title and description in cover page.
23-Feb-2016	7	Throughout document: - added TO-247 long leads package details - text and formatting changes In "Electrical ratings": - updated "Absolute maximum ratings" table. In "Electrical characteristics": - updated "Static characteristics", "IGBT switching characteristics (inductive load)" and "Diode switching characteristics (inductive load)" tables. Updated "Electrical characteristics (curves)" section. Updated "Package information" section.
07-Jun-2016	8	The part numbers STGWA40H65DFB and STGWT40H65DFB have been moved to a separate datasheet.



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