

ITAxxU1

Transil[™] array for data line protection

Features

- High surge capability Transil array: I_{PP} = 40 A (8/20 µs)
- Peak pulse power : 300 W (8/20 µs)
- Up to 6 bidirectional Transil functions
- Low clamping factor (V_{CL} / V_{BR}) at high current level
- Low leakage current
- ESD protection up to 15 kV

Complies with the following standards

- IEC 61000-4-2 level 4
 - 15 kV (air discharge)
 - 8 kV (contact discharge)
- MIL STD 883G- Method 3015-7: class 3B
 25 kV (human body model)

Applications

Data transmission lines protection, such as:

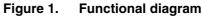
- Unipolar signal up to 5.5 V
- Bipolar signal in the ± 2.5 V range

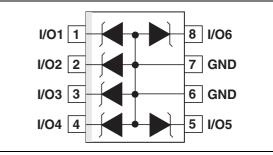
Description

Transil diode arrays provide high overvoltage protection by clamping action. Their instantaneous response to transient overvoltages makes them particularly suited to protect voltage sensitive devices such as MOS technology and low voltage supplied IC's.

The ITA series allies high surge capability against energetic pulses with high voltage performance against ESD.







TM: Transil is a trademark of STMicroelectronics

1 Characteristics

Symbol	Parameter	Value	Unit
P _{PP}	Peak pulse power (8/20 µs) ⁽¹⁾	300	W
I _{PP}	Peak pulse current (8/20 µs) ⁽¹⁾	40	А
l ² t	Wire I ² t value ⁽¹⁾	0.6	A ² s
Тj	Maximum operating junction temperature	125	°C
T _{stg}	Storage temperature range	-55 to +150	°C
ΤL	Maximum lead temperature for soldering during 10 s	260	°C

Table 1.Absolute ratings (T_{amb} = 25 °C)

1. For surges greater than the specified maximum value, the I/O will first present a short-circuit and after an open circuit caused by the wire melting.

Symbol	Parameter				1+							
V _{RM}	Stand-off voltage				IF							
V _{BR}	Breakdown voltage											
V _{CL}	Clamping voltage											
I _{RM}	Leakage current											
I _{PP}	Peak pu	ulse current										
αΤ	Voltage temperature coefficient											
V _F	Forward voltage drop					Ipp						
С	Capacitance											
	V _{BR}	@ I _R	I _{RM} @	v _{RM}	V _{CL}	@ I _{PP}	V _{CL}	@ I _{PP}	αΤ	С	V _F (@ I _F
Order code	min. (1)		max.		max. (1)	8/20 µs	max. (1)	8/20 µs	max.	max. (2)	max.	
	V	mA	μA	V	V	А	V	А	10 ⁻⁴ / °C	pF	V	А
ITA6V1U1	6.51	1	10	5	10	10	12	25	4	1500	1.3	1

Table 2.Electrical characteristics ($T_{amb} = 25 \degree C$)

1. Between I/O pin and ground.

2. Between two input pins at 0 V Bias, F = 1 MHz.



Figure 2. Pulse waveform

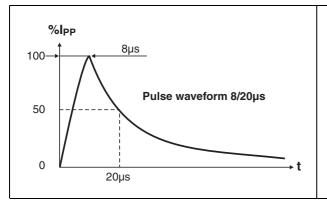
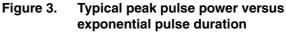


Figure 4. Clamping voltage versus peak pulse current (exponential waveform 8/20 µs)



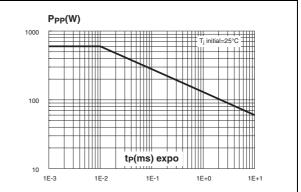
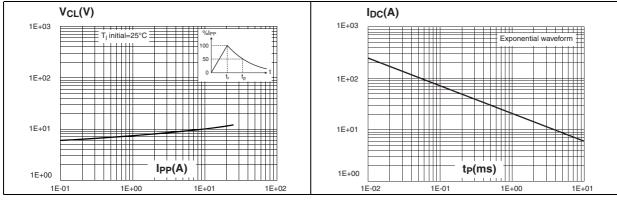


Figure 5. Peak current I_{DC} inducing open circuit of the wire for one input/output versus pulse duration (typical values)



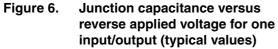
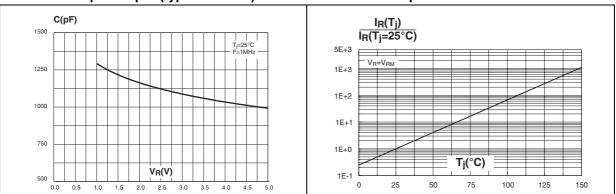
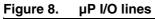
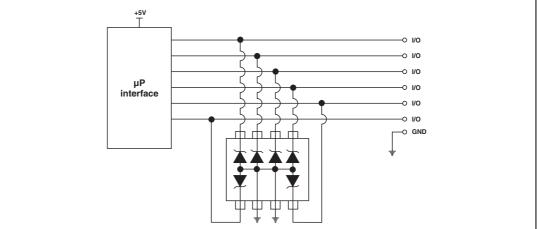


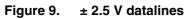
Figure 7. Relative variation of leakage current versus junction temperature

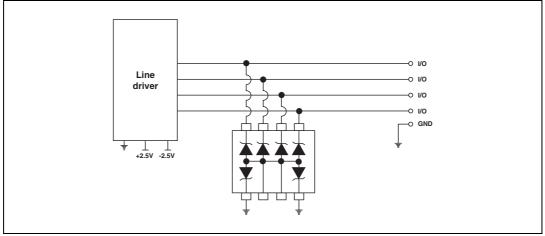


2 Application information



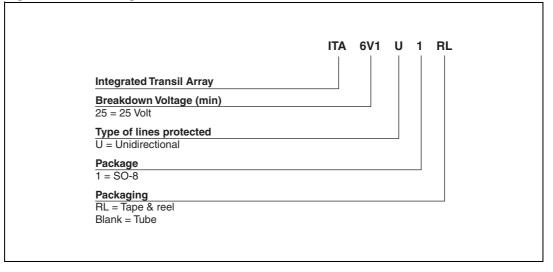






3 Ordering information scheme

Figure 10. Ordering information scheme





4 Package information

• Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Table 3. SO-8 dimensions

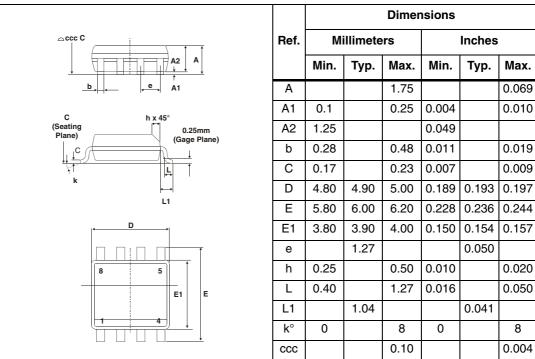
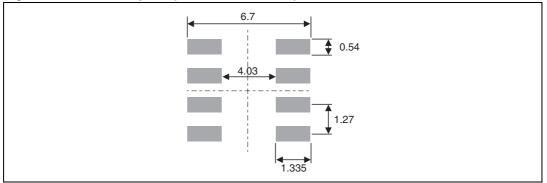


Figure 11. SO-8 footprint (dimensions in mm)



5 Ordering Information

Table 4.ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
ITA6V1U1	6V1U1	SO-8	0.08 g	2000	Tube
ITA6V1U1RL	6V1U1	30-8	0.08 y	2500	Tape and reel

6 Revision history

Table 5.Document revision history

Date	Revision	Changes
13-Dec-2004	1	Initial release.
07-Nov-2007 2		Reformatted to current standards. SO-8 package dimensions update.



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