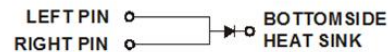
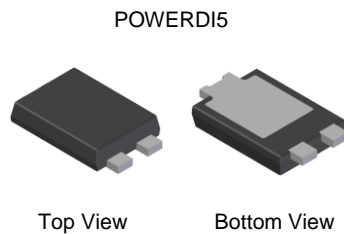


Features

- Ultra Low Forward Voltage Drop
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: POWERDI5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ③
- Polarity: See Diagram
- Weight: 0.093 grams (Approximate)



Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|----------------|----------|-------------------|
| SBR12U100P5-13 | POWERDI5 | 5,000/Tape & Reel |

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



S12U100 = Product Type Marking Code
 Jii = Manufacturers' Code Marking
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 15 for 2015)
 WW = Week Code (01 - 53)
 K = Factory Designator

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|-----------|-------|------|
| Peak Repetitive Reverse Voltage | V_{RRM} | 100 | V |
| Working Peak Reverse Voltage | V_{RWM} | | |
| DC Blocking Voltage | V_{RM} | | |
| Average Rectified Output Current (See Figure 1) | I_O | 12 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I_{FSM} | 250 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|-----------------|-------------|--------------------|
| Typical Thermal Resistance Junction to Ambient (Note 5) $T_A = +25^\circ\text{C}$ | $R_{\theta JA}$ | 27 | $^\circ\text{C/W}$ |
| Typical Thermal Resistance Junction to Lead | $R_{\theta JL}$ | 3 | $^\circ\text{C/W}$ |
| Operating and Storage Temperature Range | T_J, T_{STG} | -65 to +150 | $^\circ\text{C}$ |

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--------------------------|--------|-----|------|------|------|--|
| Forward Voltage Drop | V_F | - | 0.49 | - | V | $I_F = 5\text{A}, T_J = +25^\circ\text{C}$ $I_F = 5\text{A}, T_J = +125^\circ\text{C}$ $I_F = 12\text{A}, T_J = +25^\circ\text{C}$ |
| | | | - | 0.51 | | |
| | | | - | 0.71 | | |
| Leakage Current (Note 6) | I_R | - | - | 0.25 | mA | $V_R = 100\text{V}, T_J = +25^\circ\text{C}$ $V_R = 100\text{V}, T_J = +125^\circ\text{C}$ |
| | | | 11 | 40 | | |

Notes: 5. Device mounted on Polyimide PCB with 16x recommended pad layout.
6. Short duration pulse test used to minimize self-heating effect.

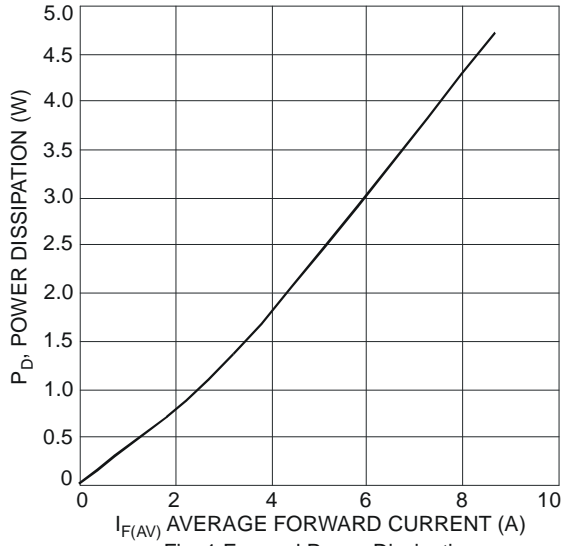


Fig. 1 Forward Power Dissipation

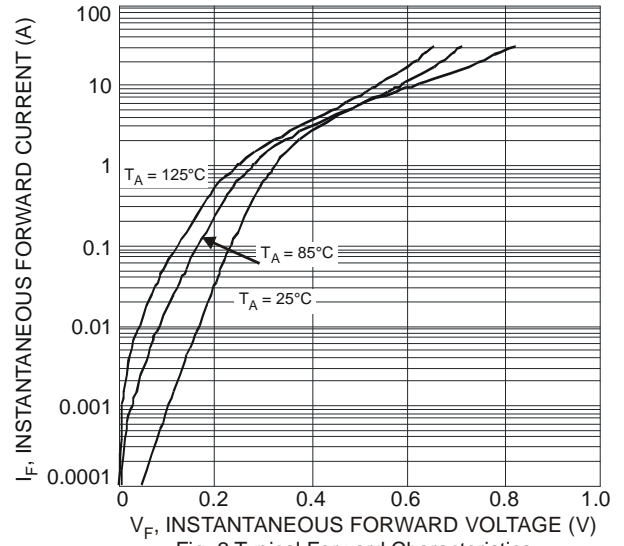


Fig. 2 Typical Forward Characteristics

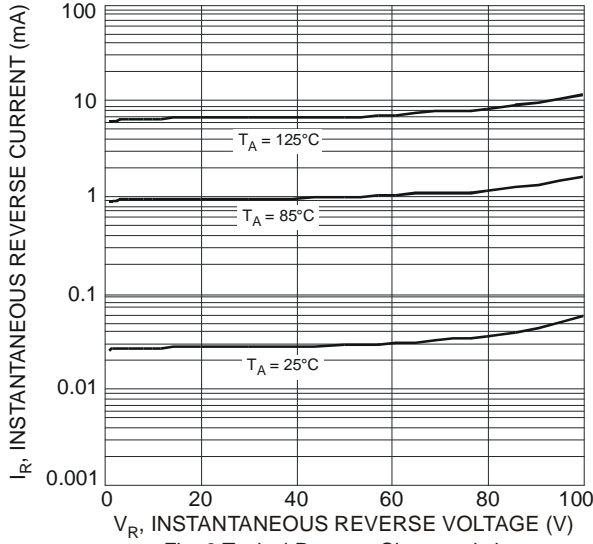


Fig. 3 Typical Reverse Characteristics

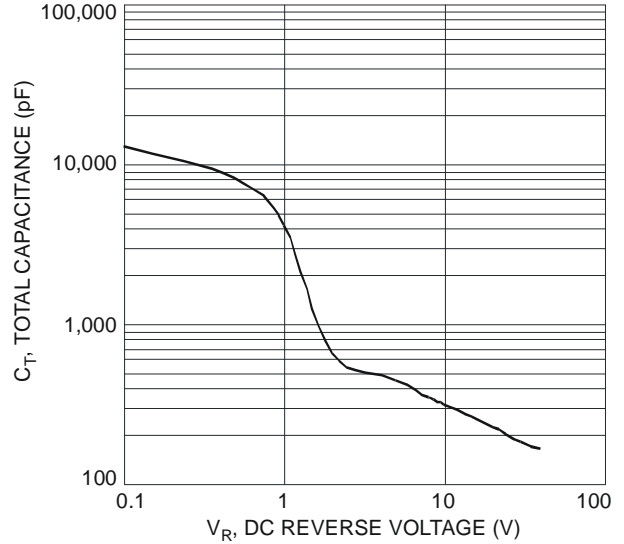


Fig. 4 Total Capacitance vs. Reverse Voltage

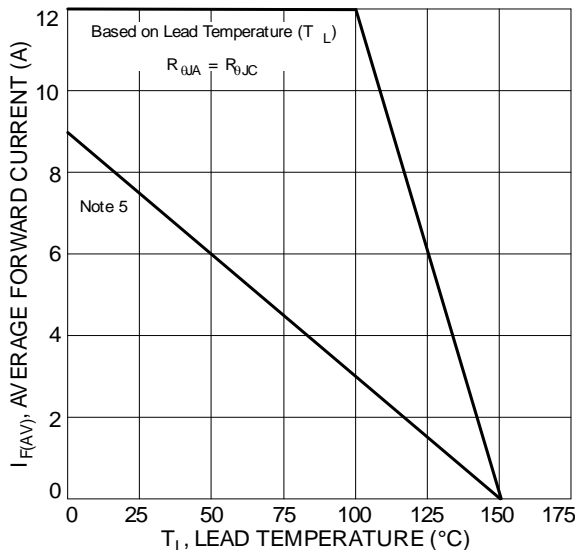


Fig. 5 Forward Current Derating Curve

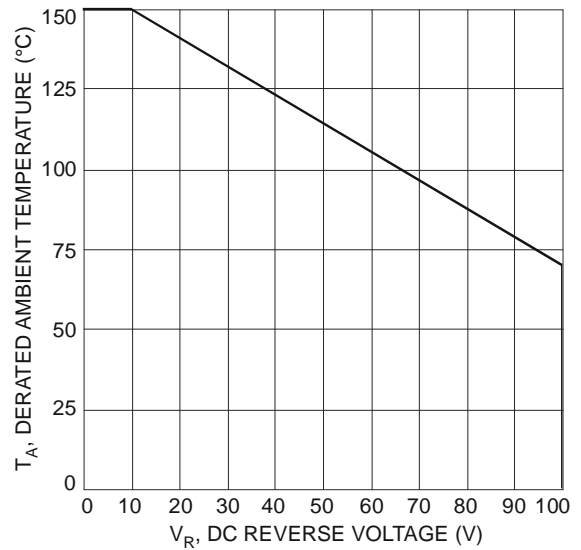
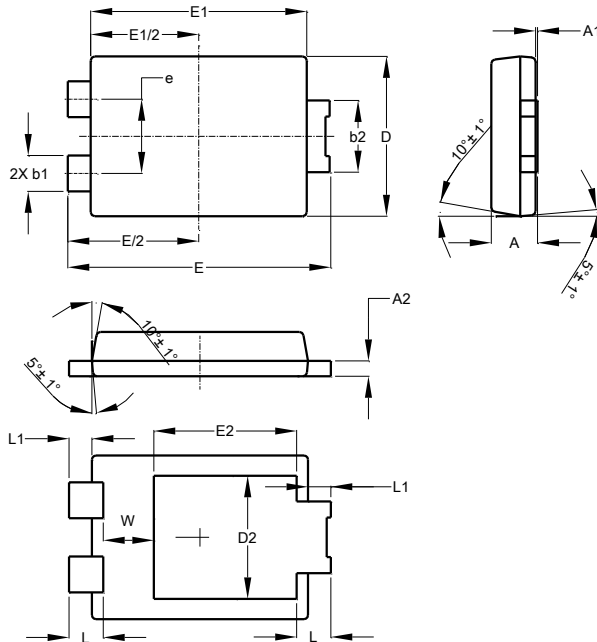


Fig. 6 Operating Temperature Derating

Package Outline Dimensions

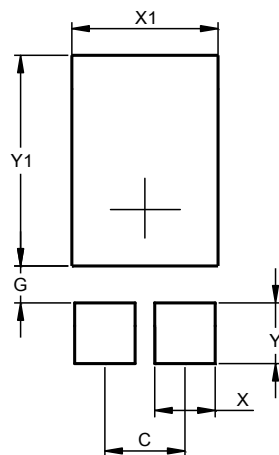
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



| POWERDI [®] 5 | | | |
|------------------------|------|------|-------|
| Dim | Min | Max | Typ |
| A | 1.05 | 1.15 | 1.10 |
| A1 | 0.00 | 0.05 | -- |
| A2 | 0.33 | 0.43 | 0.381 |
| b1 | 0.80 | 0.99 | 0.89 |
| b2 | 1.70 | 1.88 | 1.78 |
| D | 3.90 | 4.05 | 3.966 |
| D2 | -- | -- | 3.054 |
| E | 6.40 | 6.60 | 6.504 |
| e | -- | -- | 1.84 |
| E1 | 5.30 | 5.45 | 5.37 |
| E2 | -- | -- | 3.549 |
| L | 0.75 | 0.95 | 0.85 |
| L1 | 0.50 | 0.65 | 0.57 |
| W | 1.10 | 1.41 | 1.255 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 1.840 |
| G | 0.852 |
| X | 1.390 |
| X1 | 3.360 |
| Y | 1.400 |
| Y1 | 4.860 |

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